

The image features the EMemory logo in a bold, orange, lowercase sans-serif font at the top. The background is white with a pattern of light gray wireframe cubes. Some cubes are arranged in vertical columns, while others are scattered. The cubes in the columns appear to be stacked, with some overlapping. The overall aesthetic is clean and modern.

**ememory**

# **1Q 2018 Investor Conference**

**May 10<sup>th</sup>, 2018**

# IPR Notice

**All rights, titles and interests contained in this information, texts, images, figures, tables or other files herein, including, but not limited to, its ownership and the intellectual property rights, are reserved to eMemory. This information may contain privileged and confidential information. Some contents in this information can be found in Logic Non-Volatile Memory (The NVM solutions from eMemory), published in 2014. Any and all information provided herein shall not be disclosed, copied, distributed, reproduced or used in whole or in part without prior written permission of eMemory Technology Inc.**

**eMemory, NeoBit, NeoFuse, NeoFlash, NeoEE, NeoMTP, NeoROM, EcoBit and NeoPUF are all trademarks and/or service marks of eMemory in Taiwan and/or in other countries.**

# Cautionary Statement

**This presentation contains forward-looking statements, which are subject to risk factors associated with semiconductor and intellectual property business. It is believed that the expectations reflected in these statements are reasonable. But they may be affected by a variety of variables, many of which are beyond our control. These variables could cause actual results or trends to differ materially which include, but are not limited to: wafer price fluctuation, actual demand, rapid technology change, delays or failures of customers' tape-outs into wafer production, our ability to negotiate, monitor and enforce agreements for the determination and payment of royalties, any bug or fault in our technology which leads to significant damage to our technology and reputation, actual or potential litigation, semiconductor industry cycle and general economic conditions. Except as required by law, eMemory undertakes no obligation to update or revise any forward-looking statements, whether as a result of new information, future events, or otherwise.**

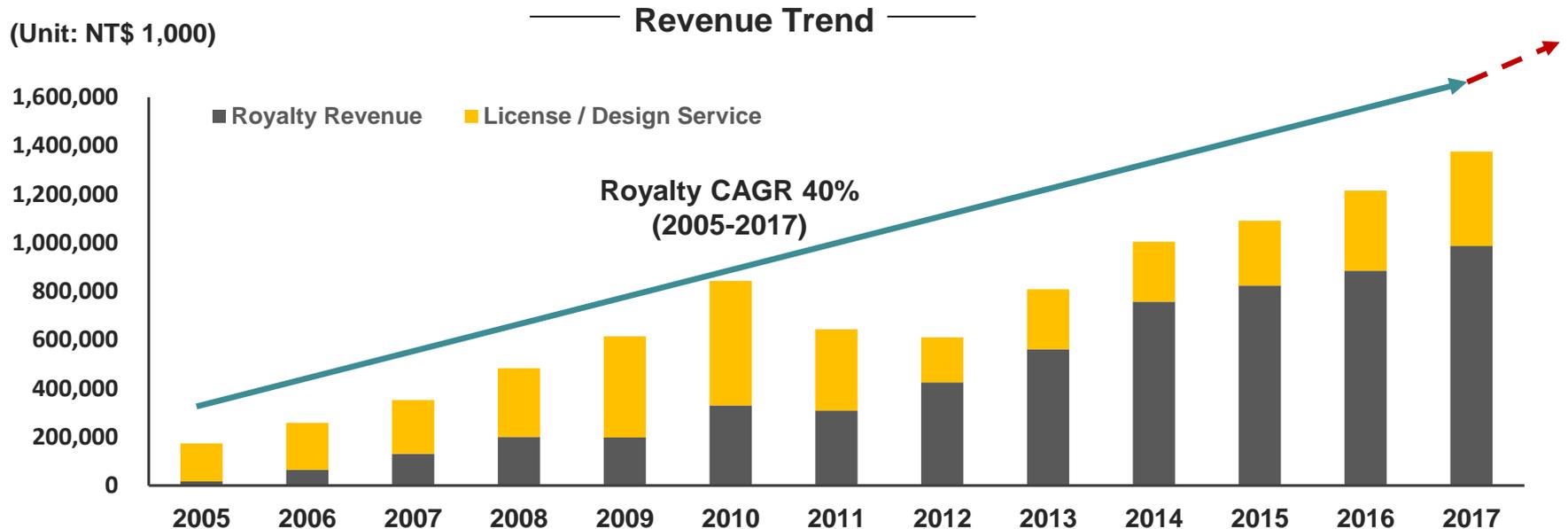
# Outline

- **Company Overview**
- **Review of Operations for 1Q 2018**
- **Security IP – NeoPUF introduction**
- **Future Outlook**
- **Q & A**

# Company Overview

eMemory is the global leader of embedded non-volatile memory IP

(Unit: NT\$ 1,000)



## Key Facts

- Headquartered in Hsinchu (Taiwan), founded in 2000, IPO in 2011
- 100% gross margins, 48.4% OP margins
- Ranking no. 7 semiconductor IP vendor
- Over 20 mlns of wafers shipped.
- Over 540 patents Issued, another 230 pending
- 244 employees (70% R&D personnel)
- Largest embedded NVM IP vendor
- TSMC Best IP Partner Award since 2010

# Worldwide Customers

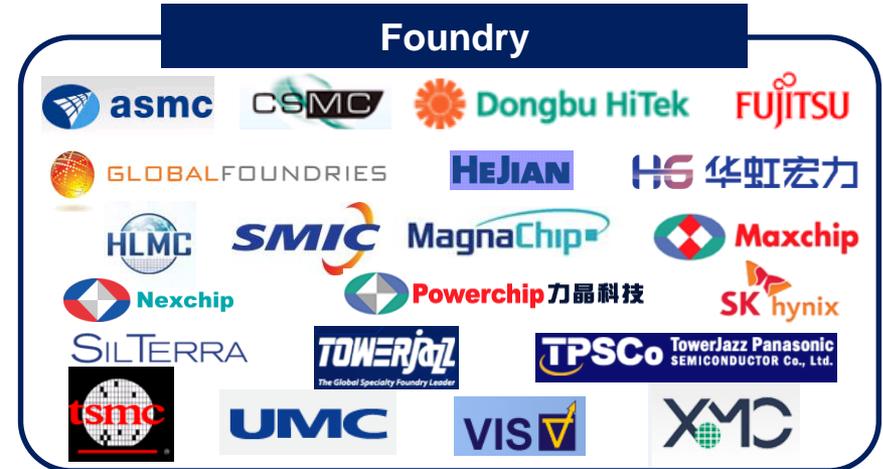
Our IP solutions are adopted by leading foundries, IDMs and fabless worldwide

## Global Customers

	Foundry	IDM	Fabless
Taiwan	5	0	259
China	8	0	512
North America	1	2	240
Europe	2	1	109
Korea	3	0	75
Japan	3	8	52
Others	1	0	53



## Foundry

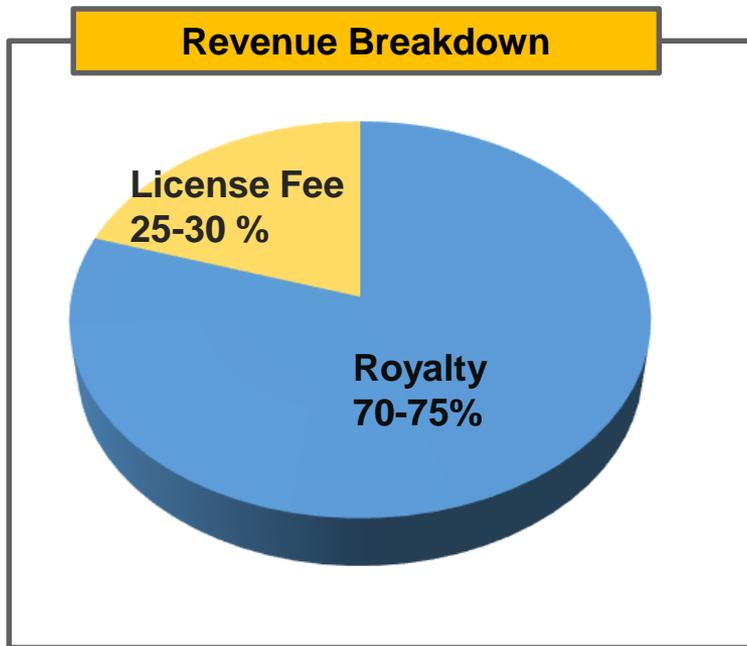


## IDM



# Business Model

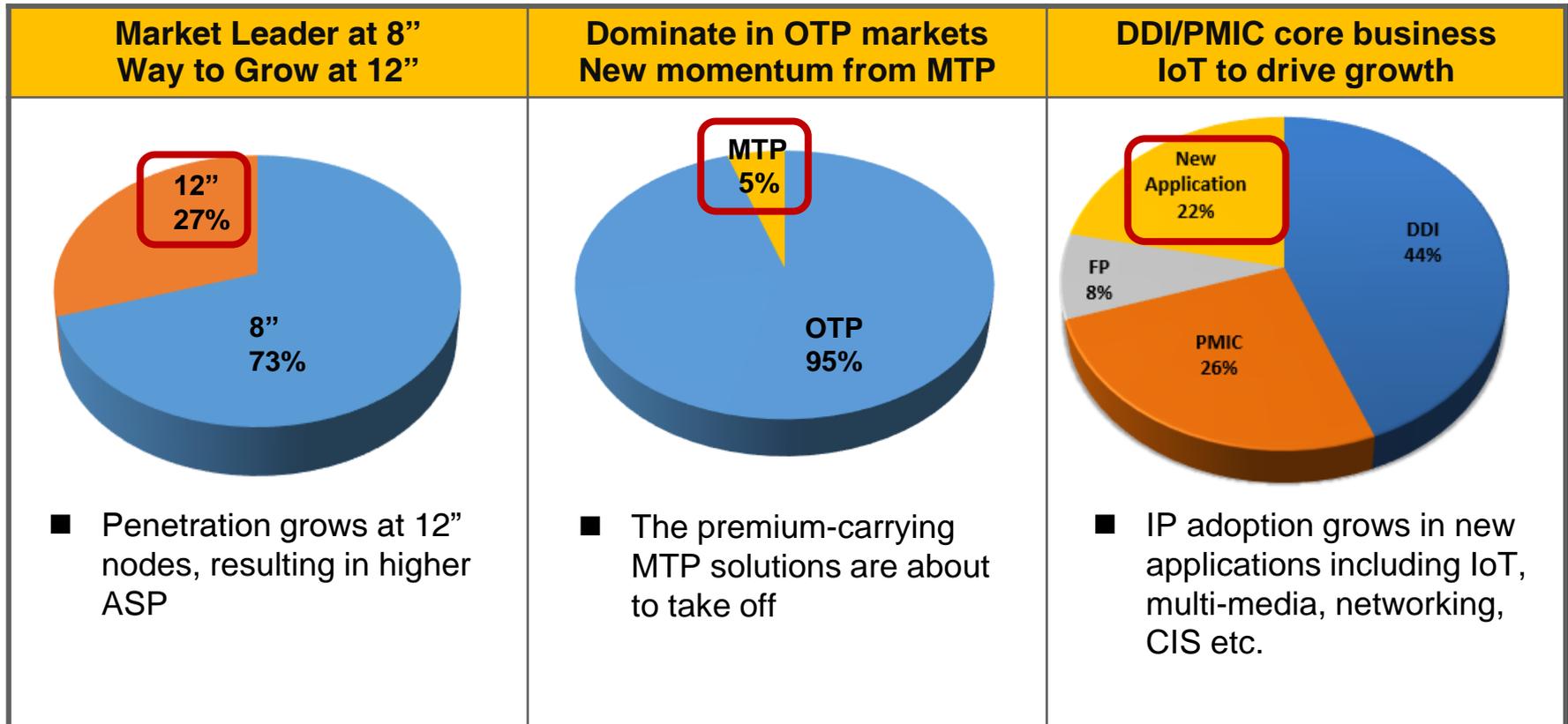
Recurring royalty is the backbone of our business



- 70-75% of revenue is from royalty based on wafer productions
- Royalty rates are based on IP type and wafer
- Royalty revenue is a key growth driver:
  - More adoption = more volume shipment
  - More advanced node wafers = Higher ASP per wafer

# Growth Engines

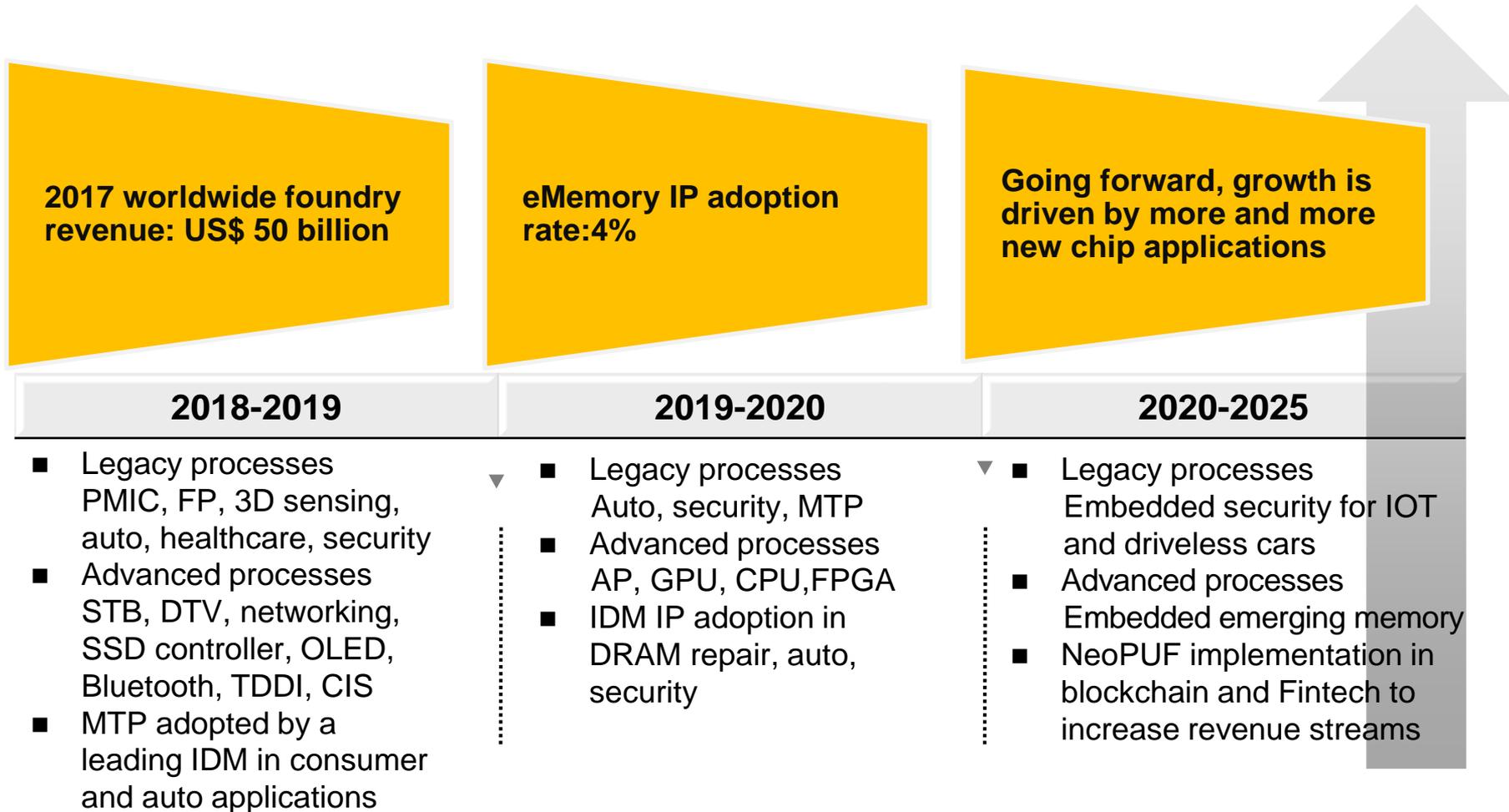
What supports our current growth, What drives our **Future Growth**



As of 2017

# Growth Prospects

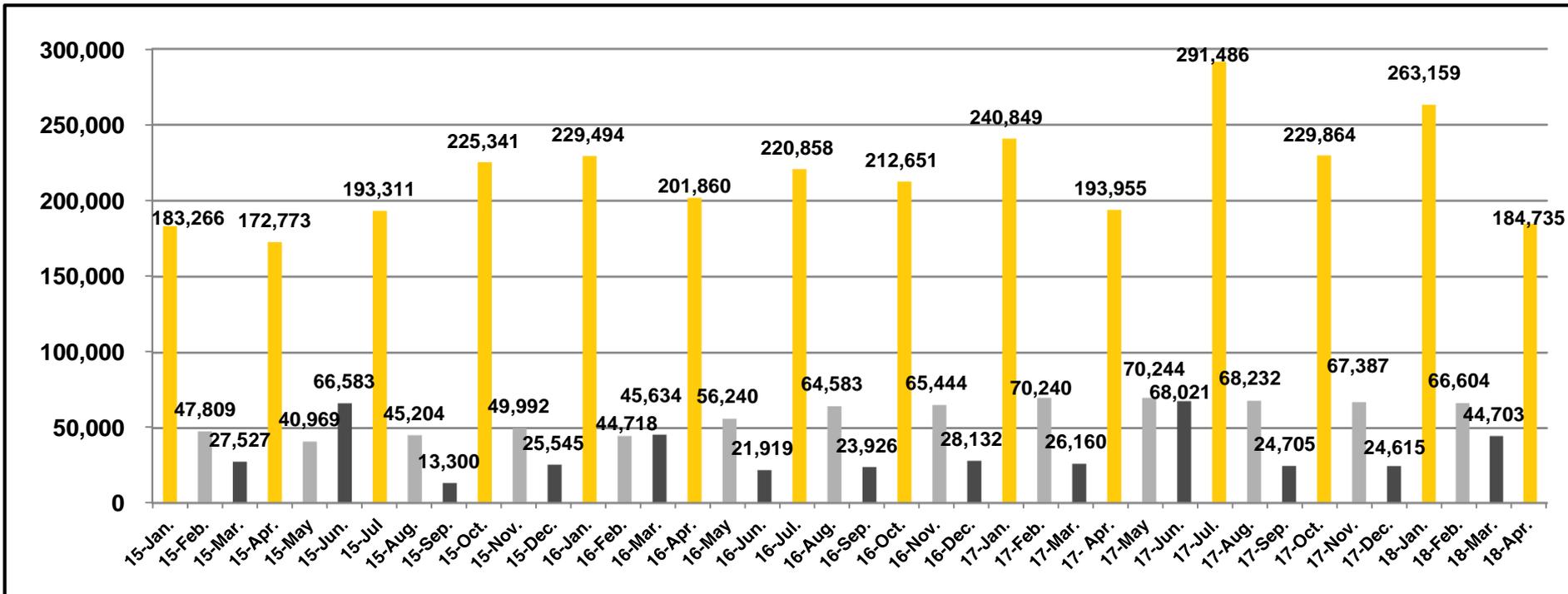
Our near-term, medium-term and long-term growth engines



# Quarterly Revenue Pattern

- 1st month: Receive **License Fees** of the month and **Royalty** from most foundries on previous quarter's wafer shipments
- 2<sup>nd</sup> month: Receive **License Fees** of the month and **Royalty** from other foundries
- 3<sup>rd</sup> month: **License Fees Only**.

Thousands of NT dollars



# Outline

- **Company Overview**
- **Review of Operations for 1Q 2018**
- **Security IP – NeoPUF introduction**
- **Future Outlook**
- **Q & A**

# Q1 Revenue Breakdown

Revenue (thousands of NT dollars)

	Q1 2018	Q4 2017	QoQ	Q1 2017	YoY	2017	2016	YoY
<b>Licensing</b>	114,540	78,811	45.3%	74,146	54.5%	388,184	330,087	17.6%
<b>Royalty</b>	259,926	243,055	6.9%	263,103	-1.2%	987,574	885,372	11.5%
<b>Total</b>	374,466	321,866	16.3%	337,249	11.0%	1,375,758	1,215,459	13.2%

Revenue (thousands of US dollars)

	Q1 2018	Q4 2017	QoQ	Q1 2017	YoY	2017	2016	YoY
<b>Licensing</b>	3,898	2,620	48.8%	2,384	63.5%	12,787	10,256	24.7%
<b>Royalty</b>	8,828	8,066	9.4%	8,366	5.5%	32,311	27,422	17.8%
<b>Total</b>	12,726	10,686	19.1%	10,750	18.4%	45,098	37,678	19.7%

Number of Licenses

		Q1 2018	Q4 2017	2017	2016
<b>Technology Licenses</b>		7	3	20	43
<b>Design Licenses</b>	<b>NRE</b>	15	15	55	56
	<b>Usage</b>	87	78	325	311

Confidential

# Financial Income Statement

Amount in Thousands of NT Dollars, except margins/EPS/ROE

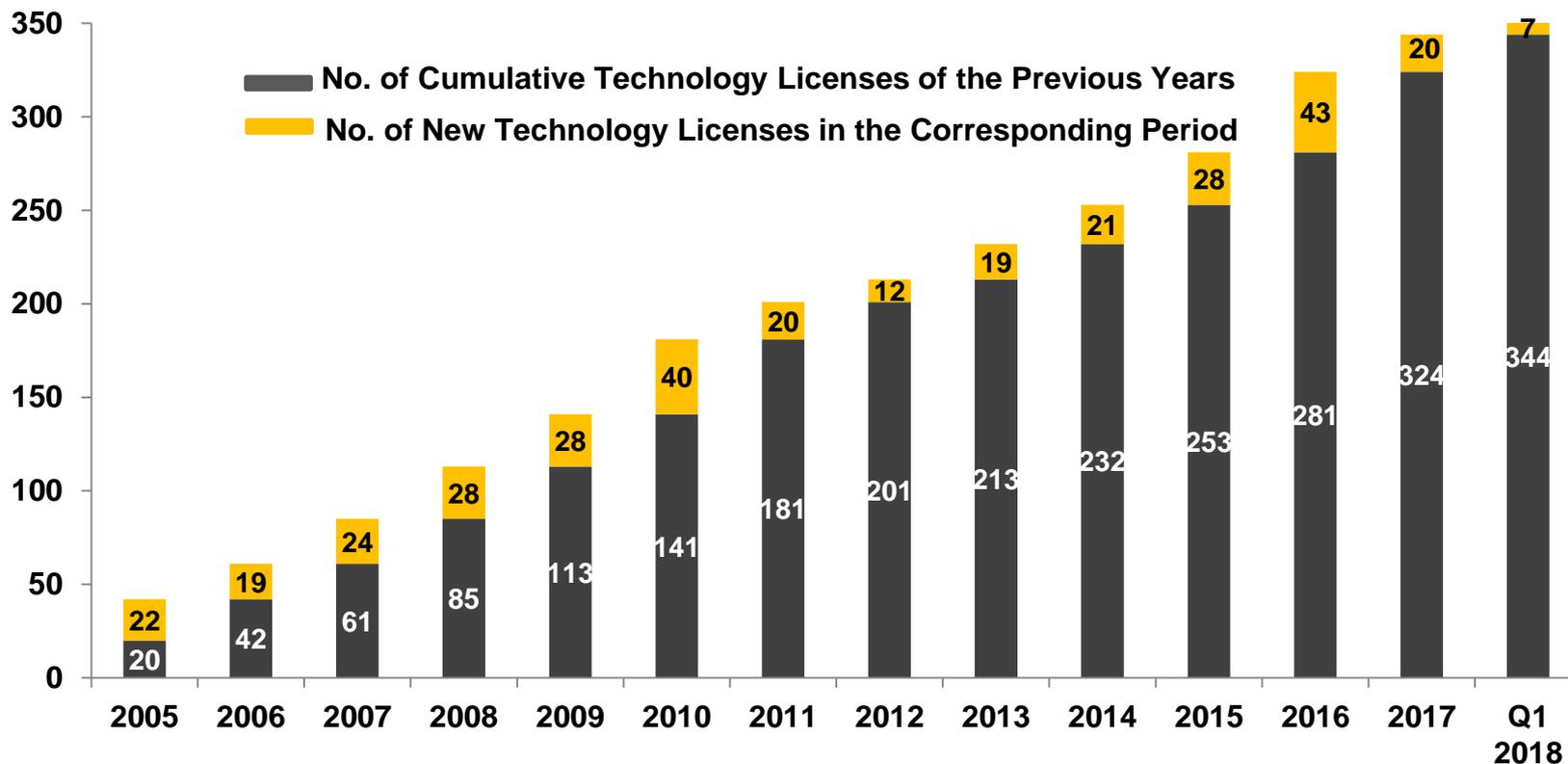
	Q1 2018	Q4 2017	Q1 2017	change (QoQ)	change (YoY)
Revenue	374,466	321,866	337,249	16.3%	11.0%
Gross Margin	100%	100%	100%	-	-
Operating Expenses	193,201	185,484	193,603	4.2%	-0.2%
Operating Margin	48.4%	42.4%	42.6%	6.0ppts	5.8ppts
Net Income	168,730	117,659	151,378	43.4%	11.5%
Net Margin	45.1%	36.6%	44.9%	8.5ppts	0.2ppts
EPS	2.23	1.55	2.00	43.9%	11.5%
ROE	31.3%	23.6%	30.2%	7.7ppts	1.1ppts

# Technology Licensing

## Number of Licenses

Year	2015	2016	2017	Q1 2018
License	28	43	20	7

Note: Terms (including number of process platforms and licensing fees) for each technology license are set contractually. Payments are made according to set milestones, and there are no particular seasonal factors involved.



# New Technologies Under Development

- New technologies being developed for **100** platforms by Q1 18.
- **15** for NeoBit, **45** for NeoFuse, **4** for NeoPUF, **11** for NeoEE, and **25** for NeoMTP.

	7/10nm	12/14/16nm	22/28nm	40nm	55/65nm	80/90nm	0.11~ 0.13um	0.15~ 0.18um
NeoBit	-	-	-	-	1	1	7	6
NeoFuse	3	3	12	6	6	9	3	3
NeoPUF	-	-	2	-	2	-	-	-
NeoEE	-	-	-	-	-	-	1	10
NeoMTP	-	-	-	1	2	2	7	13

As of March 31st, 2018

# Technology Developments by Processes

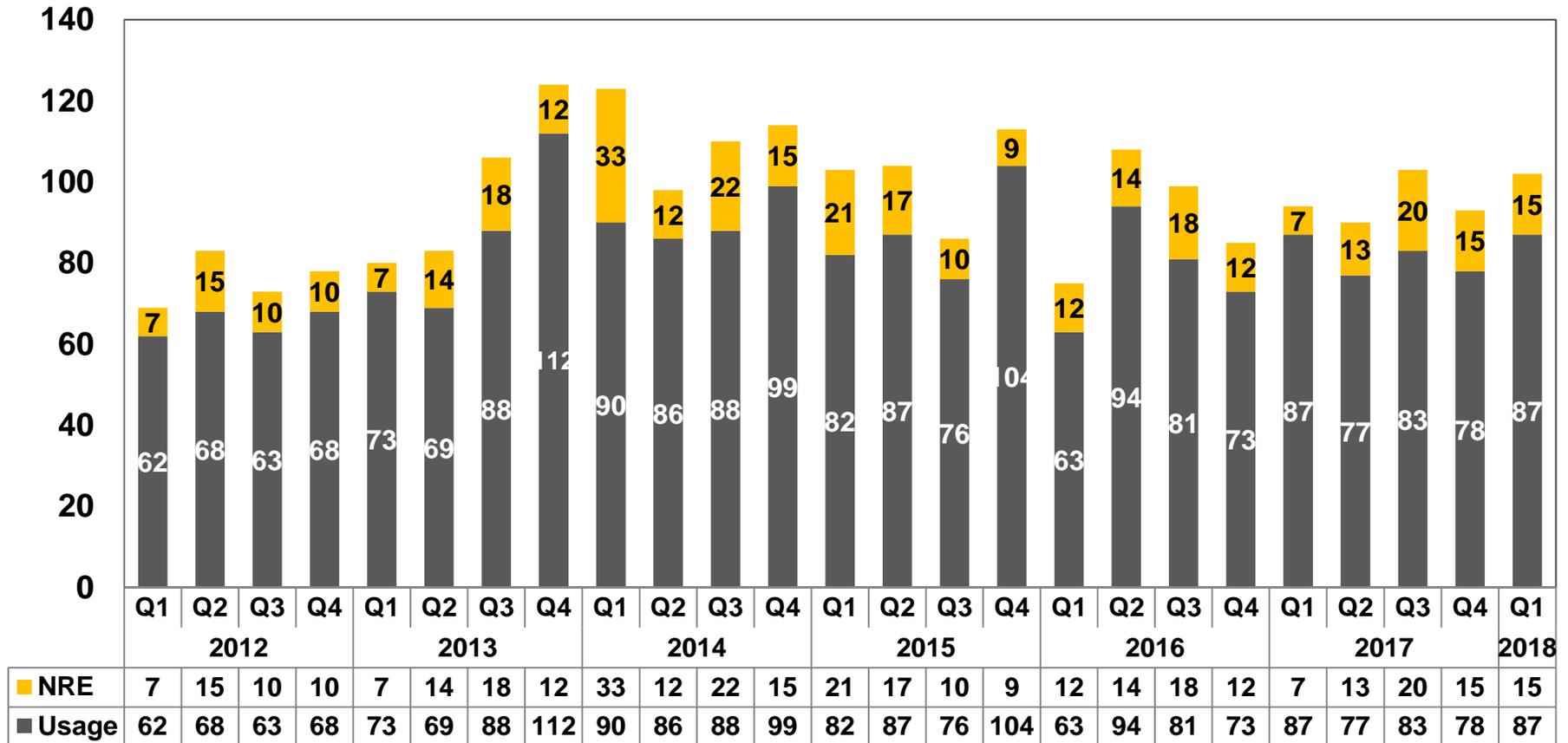
12" Fabs	Production	Development	IP Type	Process Type
7/10nm	0	3	OTP	FF
12/14/16nm	2	3	OTP	FF+
22/28nm	11	14	NeoPUF, OTP	LP/HPM, HLP/HPM, LPS, DRAM
40nm	10	7	OTP, MTP	HV-DDI, LP, eFlash
55/65nm	17	11	NeoPUF, OTP, MTP	LP, HV-DDI, HV-OLED, CIS, eFlash
80/90nm	7	9	OTP, MTP	HV-DDI, HV-OLED, LP, eFlash
0.13/0.11um	10	5	OTP, MTP	HV-DDI, BCD, Generic
0.18um	1	0	OTP	BCD
<b>Total</b>	<b>58</b>	<b>52</b>		

8" Fabs	Development	IP Type	Process Type
90nm	3	OTP	HV-DDI, LL
0.13/0.11um	13	OTP, MTP	HV-DDI, BCD, LP, RF, CIS, LL, Green
0.18/0.16/0.152um	32	OTP, MTP	Generic, LP, LL, MR, HV, Green, BCD
0.25um	0	OTP, MTP	BCD
0.35um	0	OTP	UHV
<b>Total</b>	<b>48</b>		

Note: As of March 31<sup>st</sup>, 2018

# Design Licensing (New Tape-Out)

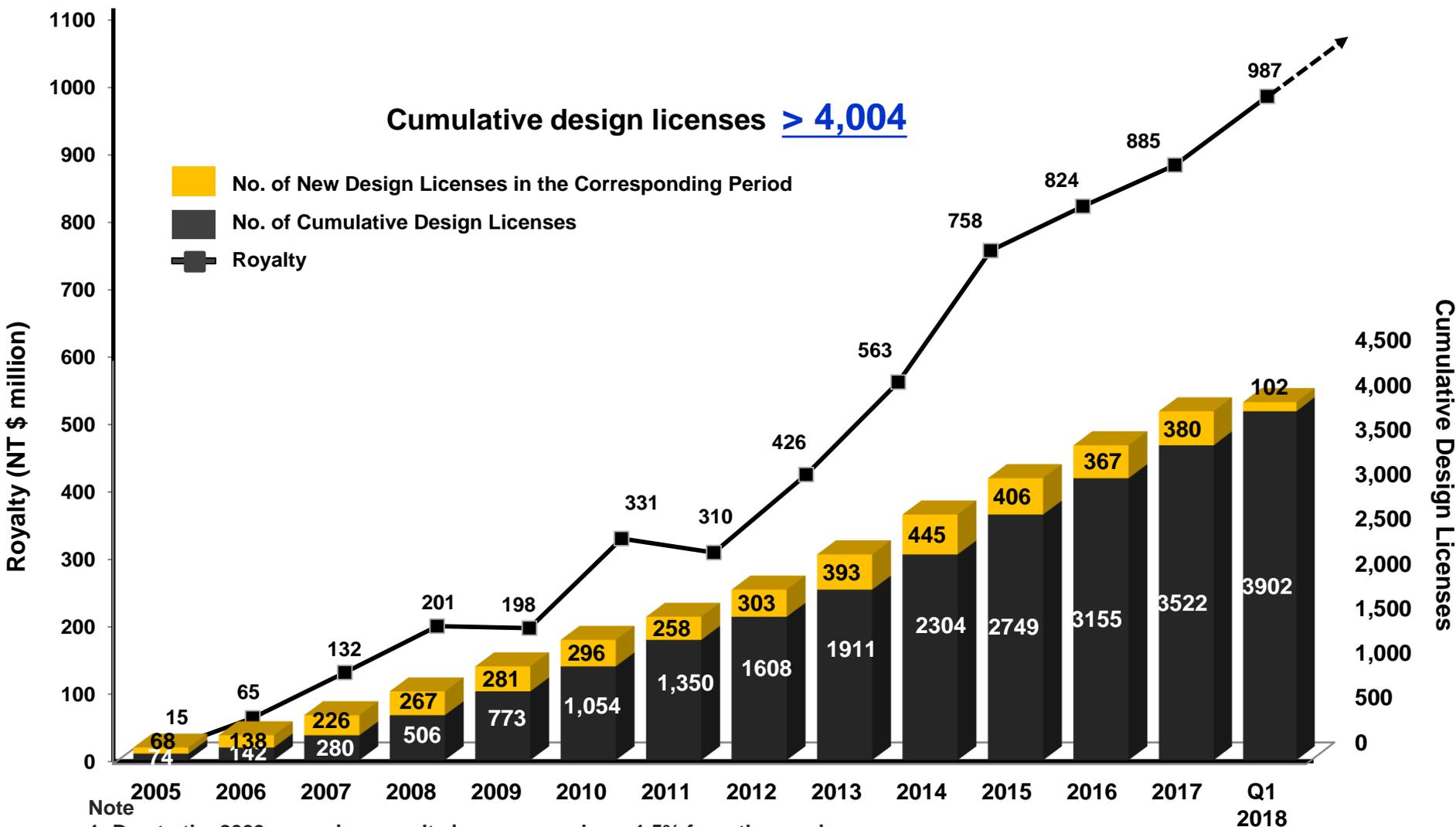
- A total **102** NTO in Q1 2018 (**380** NTO in 2017, **367**@2016, **406**@2015, **445**@2014)



Note\*: As the applications of MCU at several foundries have gradually entered mass production, and the business model of the main foundry partner which provides green process has shifted to — eMemory licenses IP cell to the foundry for it to provide direct design service to customers - as the result, the new tape out number of MCU has been affected, but the royalty coming from IP cell usage continues to roll in.

In summary, even the new tape out number of MCU is lower than before; the corresponding wafer output and royalty continue to grow.

# Cumulative Licenses Drive Future Royalties



Note

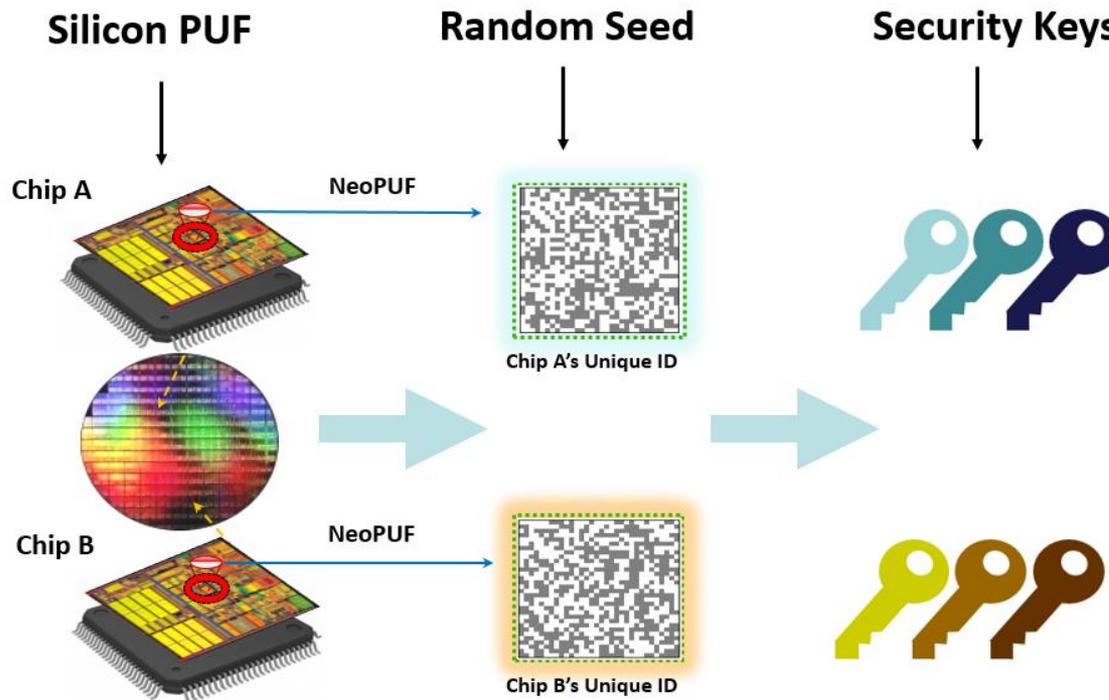
- 1: Due to the 2009 recession, royalty income was down 1.5% from the previous year.
- 2: Prepaid royalty from a single customer contributed to 2010 annual growth of 67%, followed by a drop of 6.3% in 2011.
- 3: CAGR for 2009-2013 was 30%.

# Outline

- **Company Overview**
- **Review of Operations for 1Q 2018**
- **Security IP – NeoPUF introduction**
- **Future Outlook**
- **Q & A**

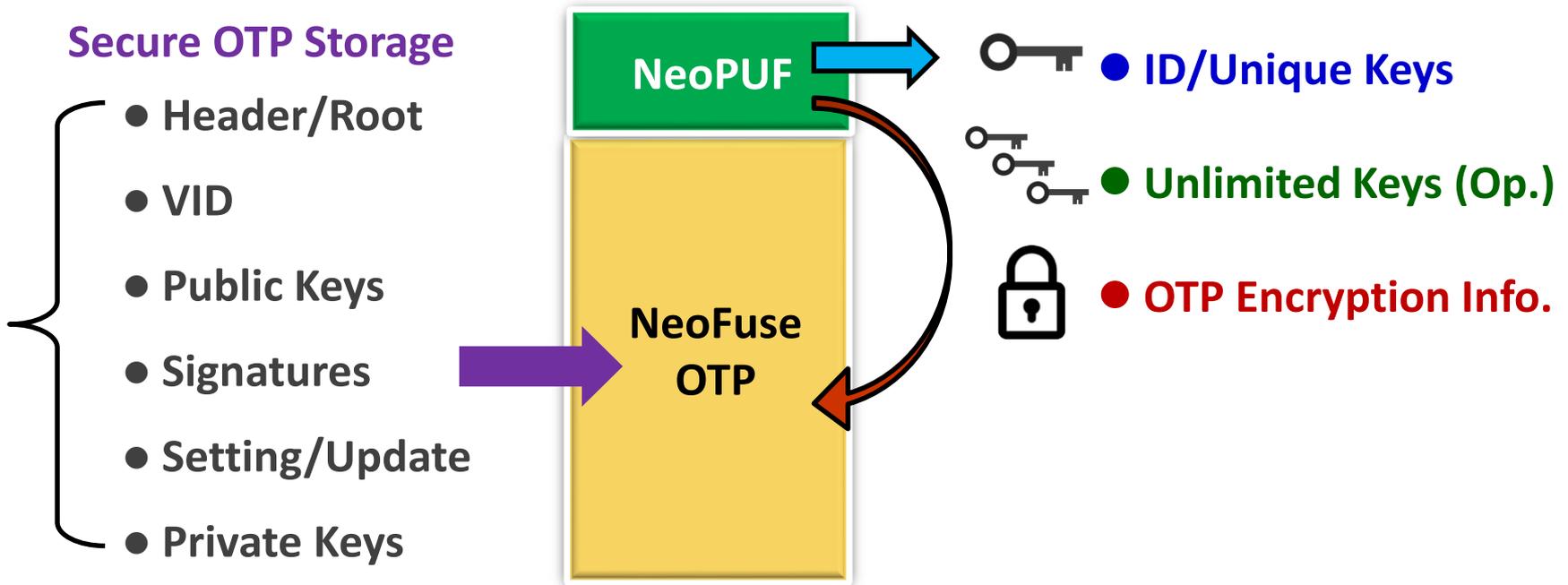
# eMemory Technology – NeoPUF

- NeoPUF is inborn silicon PUF providing unique, truly random, multiple and configurable keys for authentication or anti-counterfeits.



# Integrated NeoFuse + NeoPUF

- Solution for unique ID (secure memory) and supply chain management.



# Outline

- **Company Overview**
- **Review of Operations for 1Q 2018**
- **Security IP – NeoPUF introduction**
- **Future Outlook**
- **Q & A**

# Outlook for Q2 and beyond

- **Key drivers to licensing revenue:**
  - › **Establishing partnerships with more foundries worldwide on various processes. This year, Korea's largest foundry is expected to be added on the list. Besides that, several new license contracts beyond 16nm platform are going to be signed in this year.**
  - › **Our growing IP library will further contribute to growth of licensing revenue.**
  - › **The increasing number of MTP license contracts will drive the growth of licensing revenue in the future.**

# Outlook for Q2 and beyond

- **Key drivers to royalty revenue :**

- **8-inch processes**

- **Royalty revenue which contribute from PMIC application will grow due to the change of business terms with US largest chipmaker from one-time fee to royalty-based, which is expected to mass production this year. And, the US second largest IC Design Company has taped out and embedded our IP into its wireless charger, will contribute royalty growth in the future.**
    - **MTP technology was adopted by a European IDM company, and is ready for mass production.**
    - **Continuing to expand our market share in Fingerprint application by new customers and new applications.**
    - **Increasing design activities of application in IOT, Automotive and healthcare related.**

# Outlook for Q2 and beyond

## 12-inch processes

- **The trend that DDI applications migrate to TDDI and OLED remains unchanged, will benefit to the revenue growth of 12-inch processes.**
- **More tape-outs at 28nm for Set-top box, multimedia, SSD controllers, and Network-related applications. Major Asia design houses have adopted our IP into their 28nm products and will contribute to our royalty in 2018 and beyond.**

# Outlook for Q2 and beyond

- **Development of new applications**

- › **NeoFuse solutions have successfully designed into DRAM IDM for memory repair function, starting from 25nm and beyond, will contribute to royalty in the future.**
- › **NeoPUF is expected to work on RF, FPGA, portable POS machines, and cartridges related applications, will start contribute to royalty revenue in 2019.**

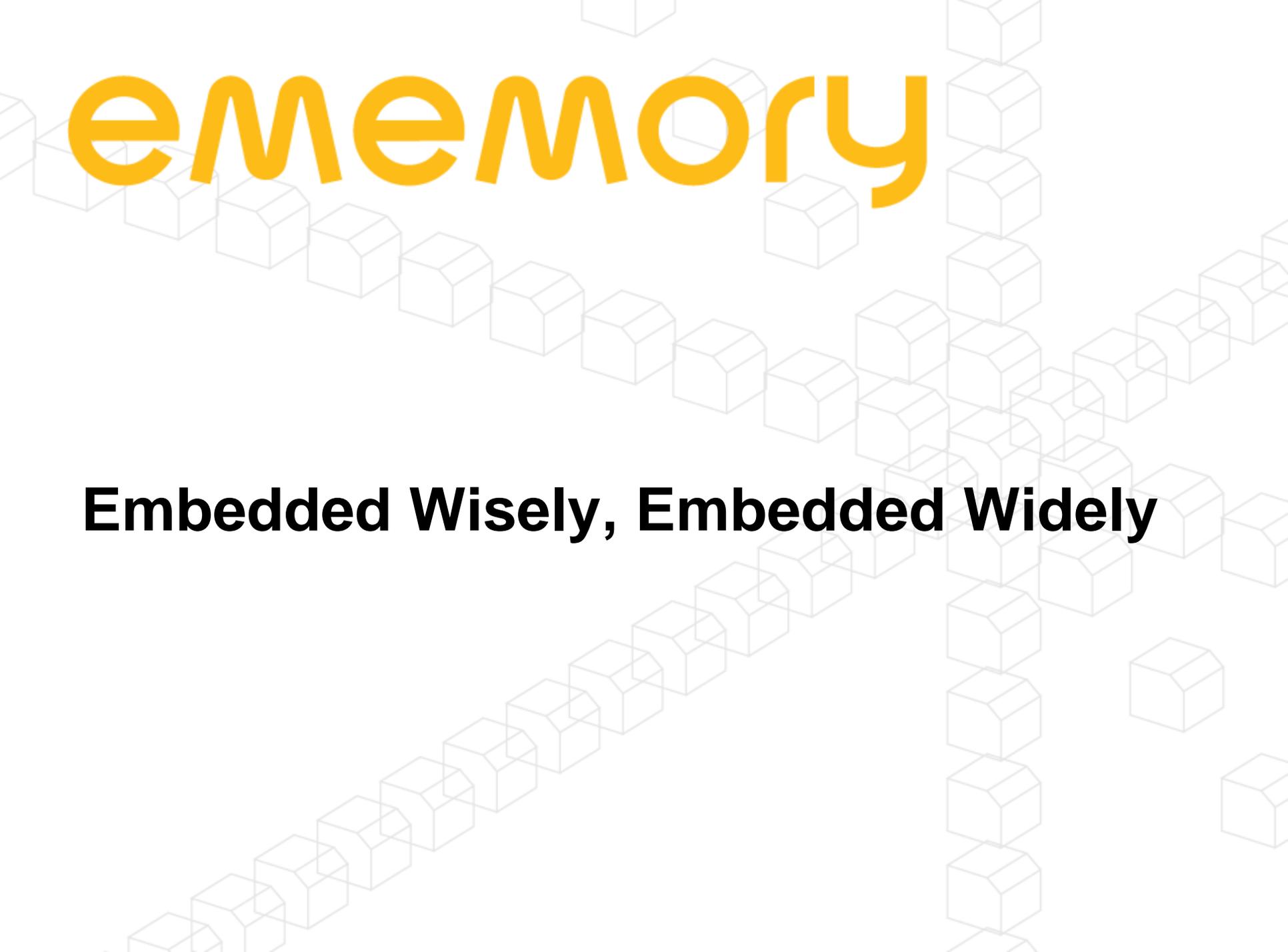
- **Development of new technologies**

- › **We constantly cooperate with main foundries in development of IPs at 5/7/12/14nm and 22nm SOI platforms.**
- › **We have started to co-develop new memory technology with a top IDM company in the US.**

# Outline

- **Company Overview**
- **Review of Operations for 1Q 2018**
- **Security IP – NeoPUF introduction**
- **Future Outlook**
- **Q & A**

# Q & A

The background of the slide is white and features a pattern of 3D cubes. Some cubes are solid light gray, while others are just wireframe outlines. They are arranged in various orientations and positions, creating a sense of depth and a grid-like structure.

# eMemory

**Embedded Wisely, Embedded Widely**