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Investor Presentation

eMemory Technology Inc.

January 2018

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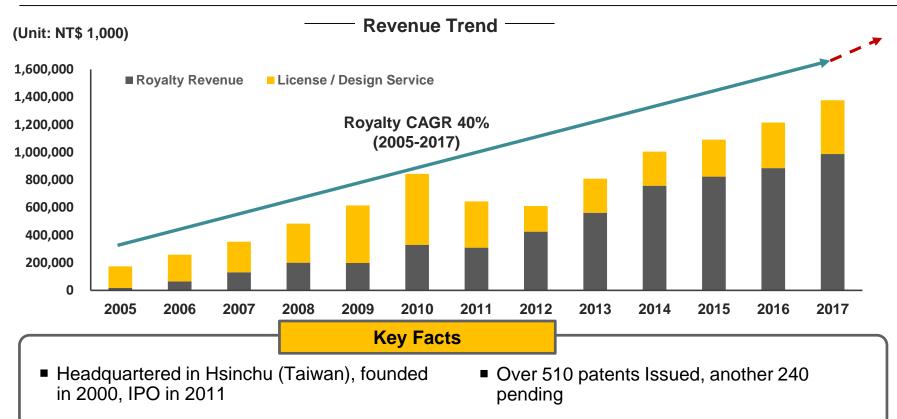
eMemory – the Embedded IP Expert

- Key Summary
- Value Creations
- Financials



Company Overview

eMemory is the global leader of embedded non-volatile memory IP



- 100% gross margins, 48.6% OP margins
- Ranking no. 7 semiconductor IP vendor
- Over 18.5 mlns of wafers shipped.

- 238 employees (70% R&D personnel)
- Largest embedded NVM IP vendor
- TSMC Best IP Partner Award since 2010

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Worldwide Customers

Our IP solutions are adopted by leading foundries, IDMs and fabless worldwide

Global Customers Foundry IDM **Fabless** 5 0 261 Taiwan 513 China 8 0 North America 1 2 242 2 111 Europe 1 3 71 Korea 0 52 8 Japan 4 53 Others 0 1

Foundry asmc 🗱 Dongbu HiTek FUĬĬTSU GLOBALFOUNDRIES HEJIAN **HG** 华虹宏力 SMIC MagnaChip 🚺 Maxchip Powerchip力晶科技 Nexchip SK hynix SILTERRA Co TowerJazz Panasonic SEMICONDUCTOR Co., Ltd. UMC VIS

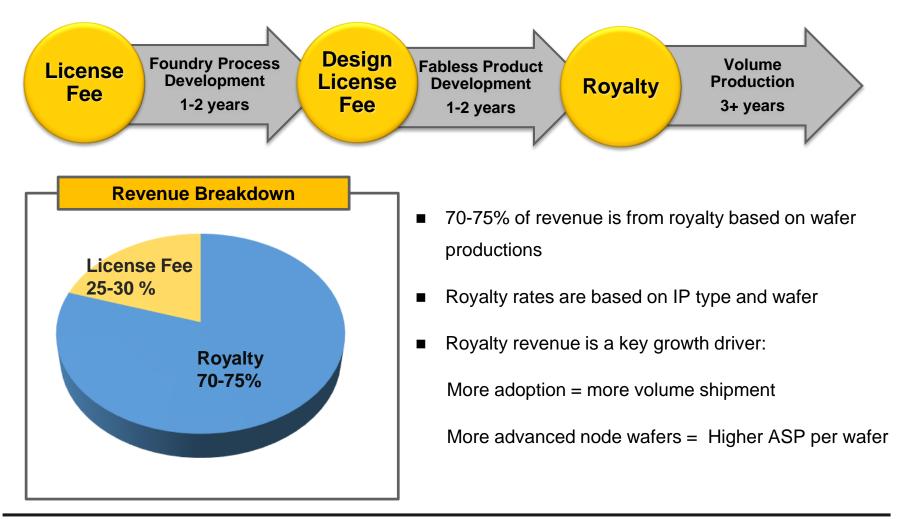


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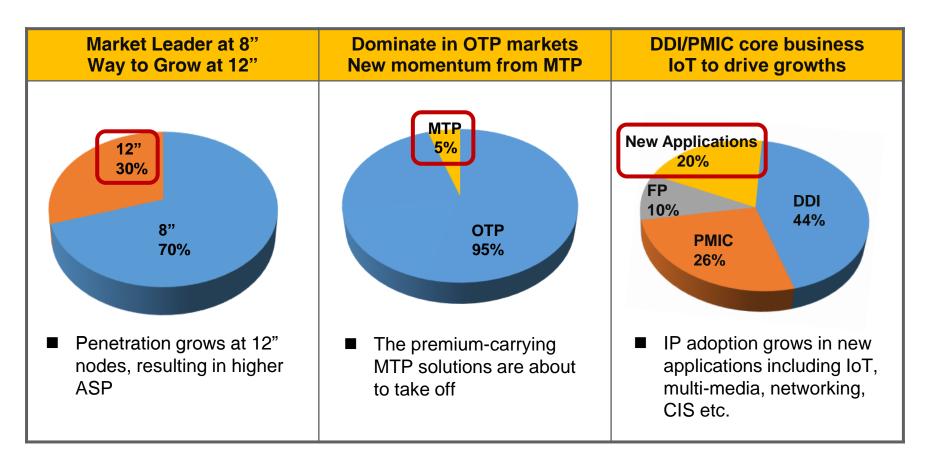
Business Model

Recurring royalty is the backbone of our business



Growth Engines

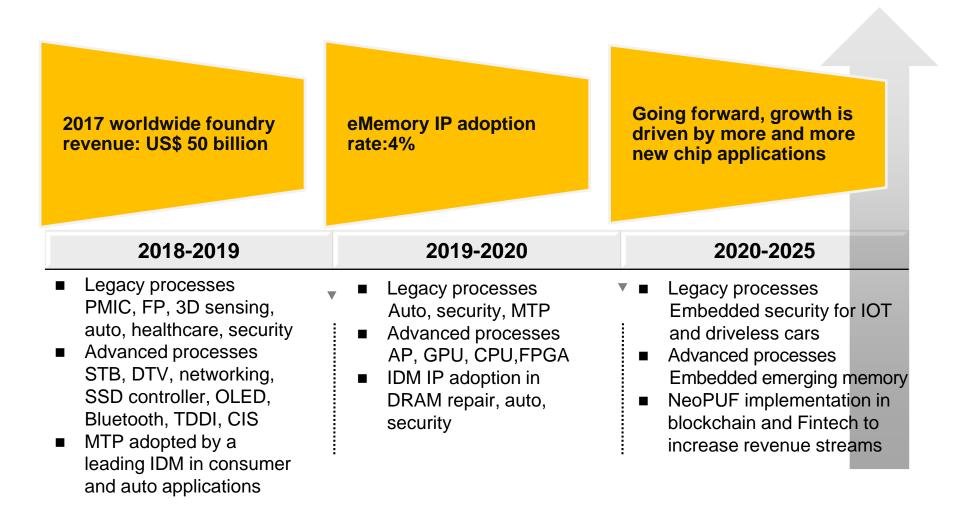
What supports our current growths, What drives our Future Growths



As of Q317

Growth Prospects

Our near-term, medium-term and long-term growth engines





eMemory – the Embedded IP Expert

• Key Summary

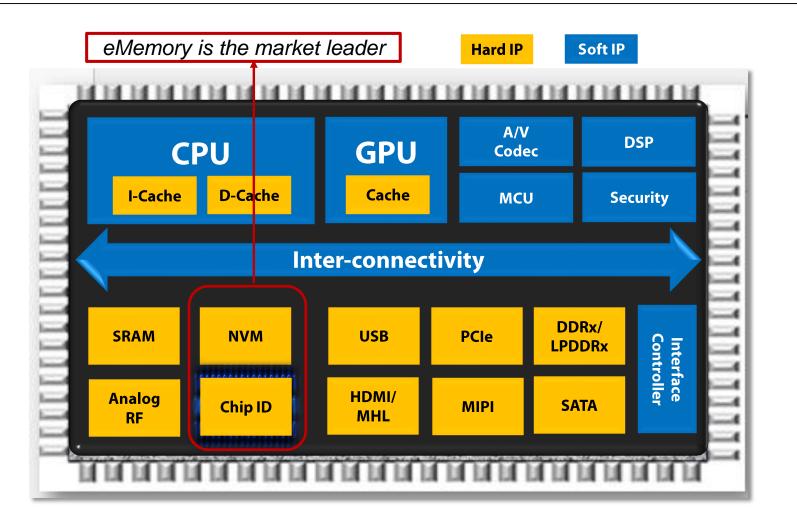
Value Creations

• Financials



Leading Silicon IP Provider

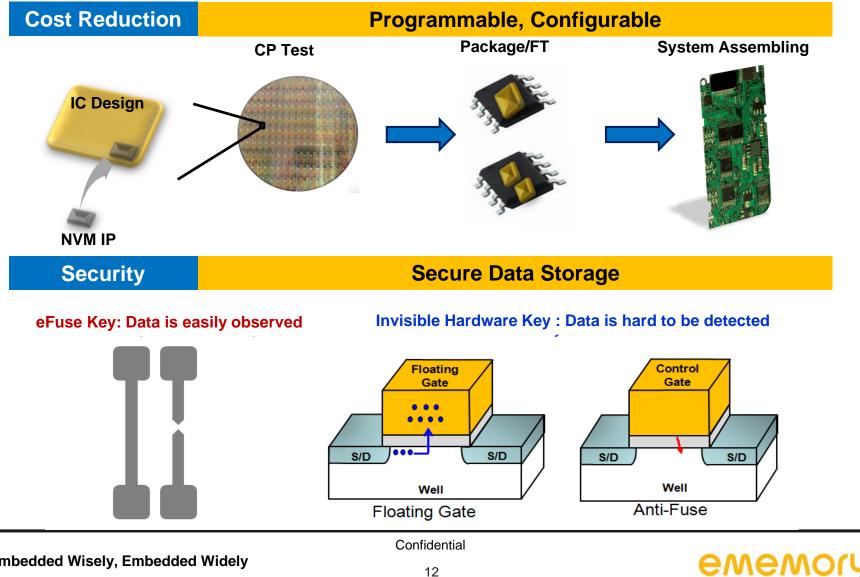
eMemory's embedded Non-Volatile Memory and Chip ID are foundation IP of SoC





Embedded NVM Usage

eMemory's embedded NVM creates values to customers



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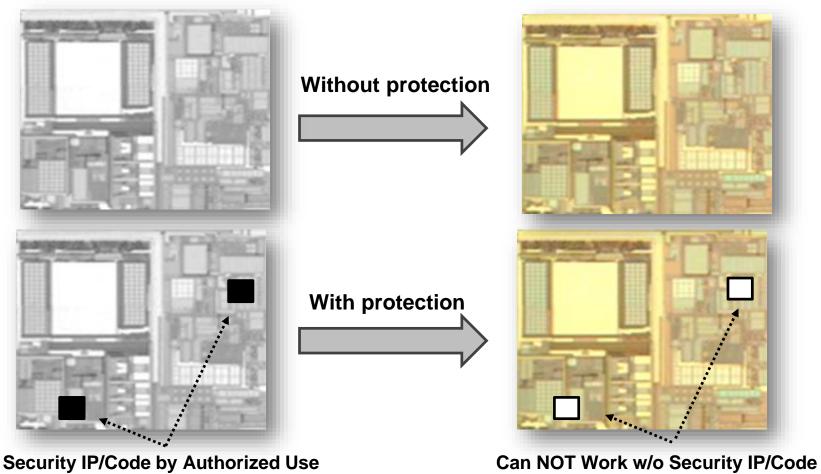
Embedded NVM Usage

Security

Secure Data Storage

Fake Product

Authorized Product



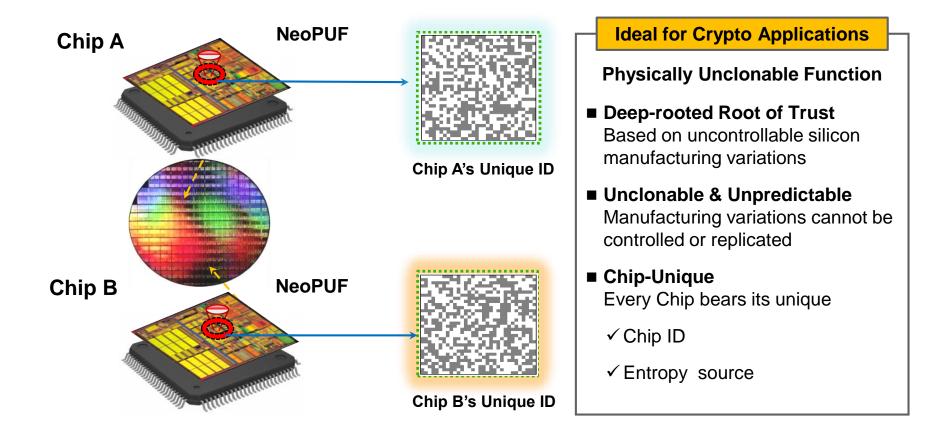
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Technology that Defines the Future

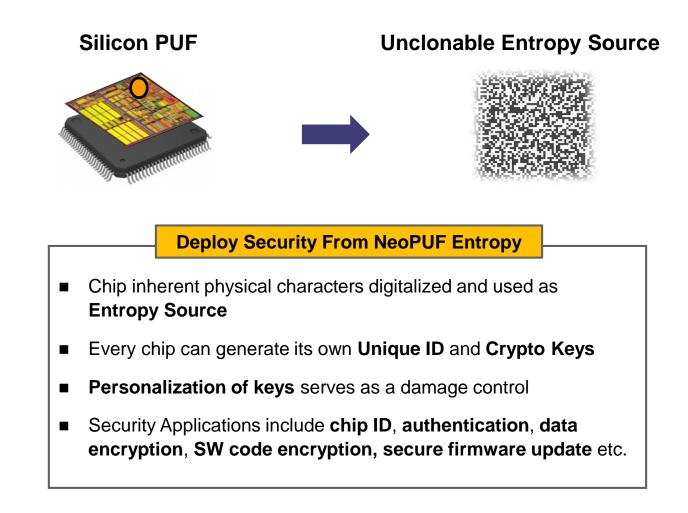
eMemory's Chip ID is the security element in every emerging big trends



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Secure HW& SW from Chip-level

eMemory's chip ID provides the fundamental security for every single chip





Global Security Market Forecast

eMemory's NeoPUF is set to capitalize on the promising new markets



IoT/IIoT



Fintech



Connected Car

By 2027 global IoT security market is forecast

US\$48 billion in revenue*

CAGR of 14.9% (2017-2027)



Data Center



Chip Vendor/OEM IP Protection

'Internet of Things (IoT) Security Product Market: Global Industry Analysis 2012-2016 and Opportunity Assessment 2017-2027', Future Market Insight



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Financial Income Statement

Amount in Thousands of NT Dollars, except margins/EPS/ROE

	Q3 2017	Q2 2017	Q3 2016	change (QoQ)	change (YoY)
Revenue	384,423	332,220	309,367	15.7%	24.3%
Gross Margin	100%	100%	100%	-	-
Operating Expenses	205,291	188,562	173,605	8.9%	18.3%
Operating Margin	46.6%	43.2%	43.9%	3.4ppts	2.7ppts
Net Income	194,062	135,610	130,299	43.1%	48.9%
Net Margin	50.5%	40.8%	42.1%	9.7ppts	8.4ppts
EPS	2.56	1.79	1.72	43.0%	48.8%
ROE	40.2%	29.6%	28.9%	10.6ppts	11.3ppts

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Q4 Revenue Breakdown

Revenue (thousands of NT dollars)

K NTD	Q4 2017	Q3 2017	QoQ	Q4 2016	YoY	2017	2016	ΥοΥ
Licensing	78,811	101,087	-22.04%	79,684	-1.10%	388,184	330,087	17.60%
Royalty	243,055	283,336	-14.22%	226,543	7.29%	987,574	885,372	11.54%
Total	321,866	384,423	-16.27%	306,227	5.11%	1,375,758	1,215,459	13.19%

Number of Licenses

		Q4 2017	Q3 2017	2017	2016
Technolog	gy Licenses	3	4	20	43
Design	NRE	15	23	55	56
Licenses	Usage	78	87	325	311

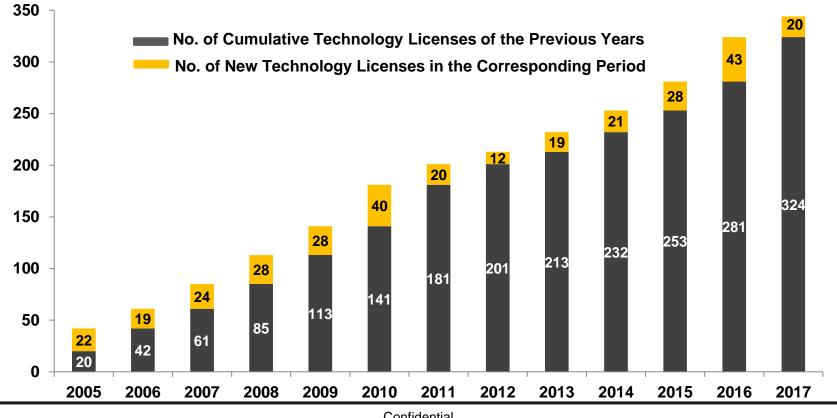


Technology Licensing

Number of Licenses

Year	2014	2015	2016	2017
License	21	28	43	20

Note: Terms (including number of process platforms and licensing fees) for each technology license are set contractually. Payments are made according to set milestones, and there are no particular seasonal factors involved.



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New Technologies Under Development

- New technologies being developed for 106 platforms by Q4 17.
- 18 for NeoBit, 40 for NeoFuse, 3 for NeoPUF, 18 for NeoEE, and 27 for NeoMTP.

	7/10nm	12/14/16nm	28nm	40nm	55/65nm	80/90nm	0.11~ 0.13um	0.15~ 0.18um	>0.25 um
NeoBit	-	-	-	-	1	-	7	10	
NeoFuse	3	3	10	4	8	7	1	4	-
NeoPUF	-	-	1	-	2	-	-	-	-
NeoEE	-	-	-	-	-	-	2	16	-
NeoMTP	-	-	-	1	2	2	7	15	-

As of Dec. 31st, 2017



Technology Developments by Processes

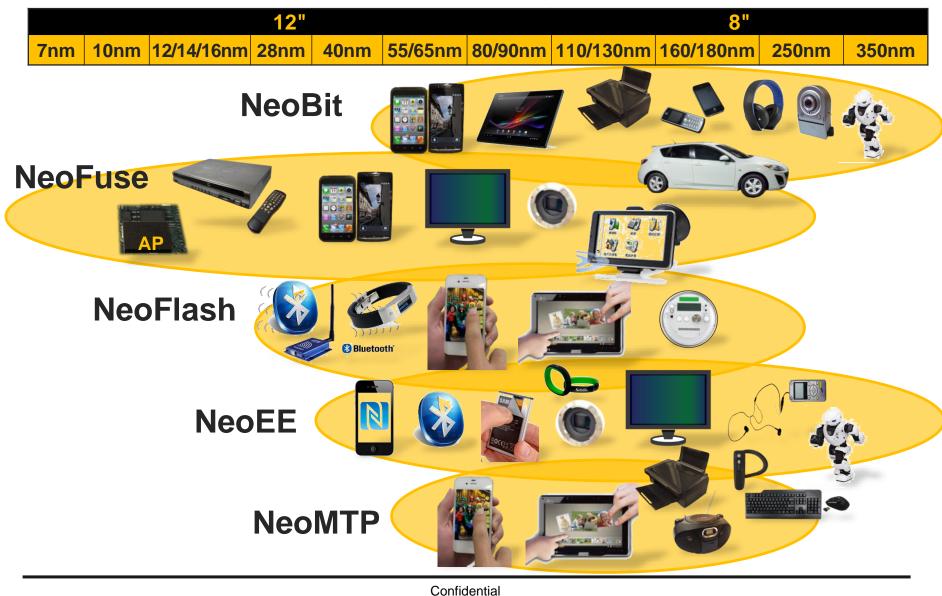
12" Fabs	Production	Development	NVM Type	Process Type
7/10nm	0	3	ОТР	FF
14/16nm	2	3	ОТР	FF+
28nm	10	11	ОТР	LP/HPM, HLP/HPM, LPS
40nm	10	5	OTP, MTP	HV-DDI, LP, eFlash
55/65nm	15	13	OTP, MTP	LP, HV-DDI, HV-OLED, DRAM, CIS, eFlash
80/90nm	7	6	OTP, MTP	HV-DDI, HV-OLED, LP, eFlash
0.13/0.11um	9	2	ОТР	HV-DDI, BCD, Generic
0.18um	1	0	ОТР	BCD
Total	54	43		

8" Fabs	Development	NVM Type	Process Type
90nm	3	ОТР	HV-DDI, LL
0.13/0.11um	15	OTP, MTP	HV-DDI, BCD, LP, RF, CIS, LL, Green
0.18/0.16/0.152um	45	OTP, MTP	Generic, LP, LL, MR, HV, Green, BCD
0.25um	0	OTP, MTP	BCD
0.35um	0	ОТР	UHV
Total	63		

Note: As of Dec. 31st, 2017



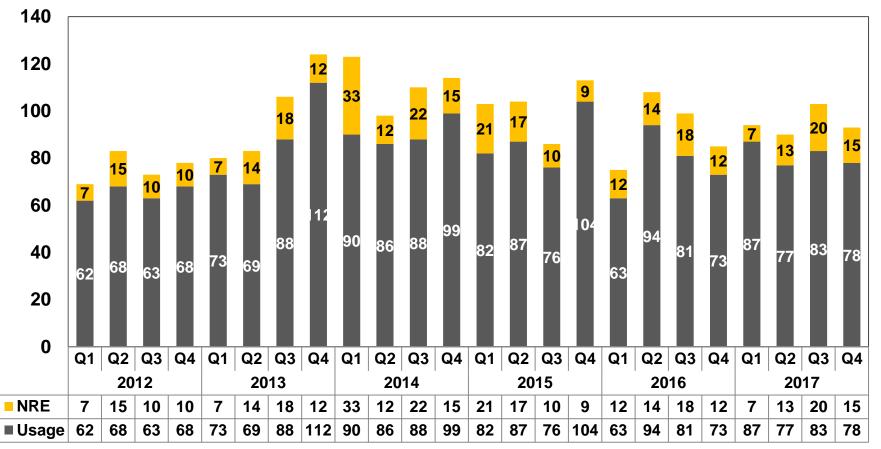
Application Markets





Design Licensing (New Tape-Out)

- A total 380 NTO in Q1-Q3 2017 (367@2016, 406@2015, 445@2014, 393@2013)



Note*: As the applications of MCU at several foundries have gradually entered mass production, and the business model of the main foundry partner which provides green process has shifted to — eMemory licenses IP cell to the foundry for it to provide direct design service to customers as the result, the new tape out number of MCU has been affected, but the royalty coming from IP cell usage continues to roll in. In summary, even the new tape out number of MCU is lower than before; the corresponding wafer output and royalty continue to grow.

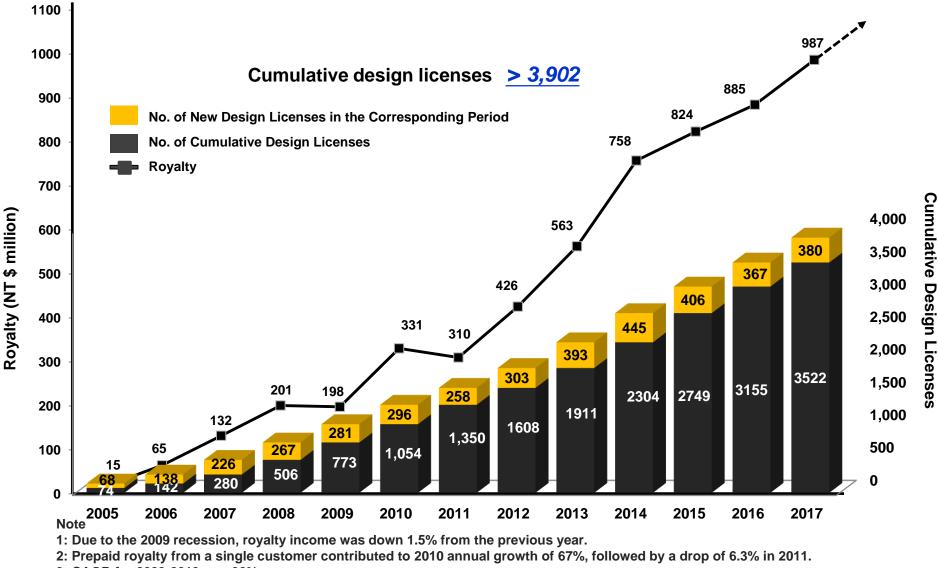
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Cumulative Licenses Drive Future Royalties



3: CAGR for 2009-2013 was 30%.

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