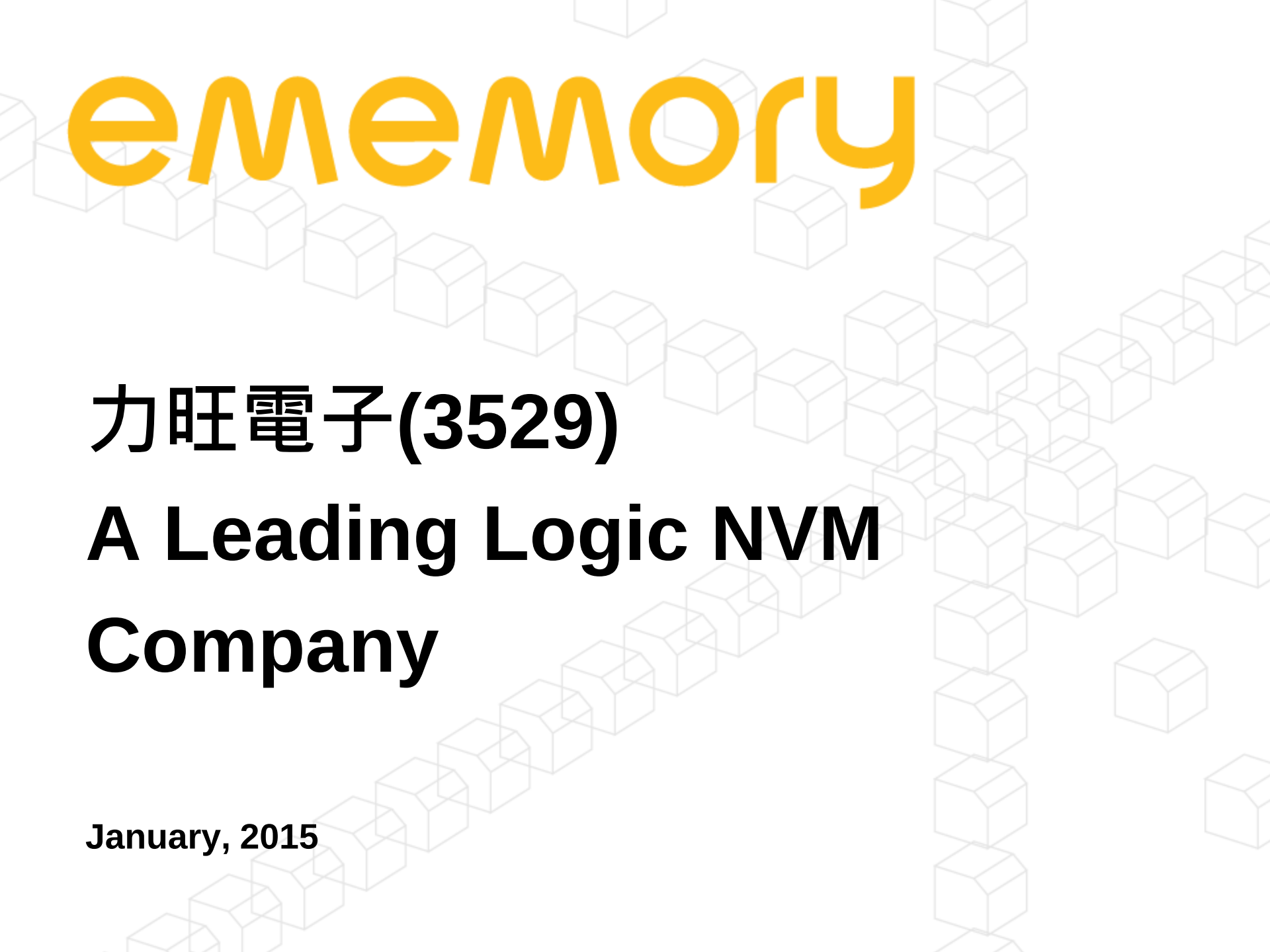




ememory

力旺電子(3529)

**A Leading Logic NVM
Company**

January, 2015

智慧財產權聲明

本文件內之資訊，包括文字、圖片、圖表、表格或其他檔案等，其所有權利或利益，包括但不限於所有權及智慧財產權，皆屬力旺電子所有，請尊重智慧財產權。本文件之內容包含力旺電子之機密資訊。部分內容可參見2014年出版之Logic Non-Volatile Memory (The NVM solutions from eMemory)一書。任何在此之資訊在未經力旺電子書面同意，不得影印、散佈、複製、使用本文件或將其揭露予第三人。

投資安全聲明

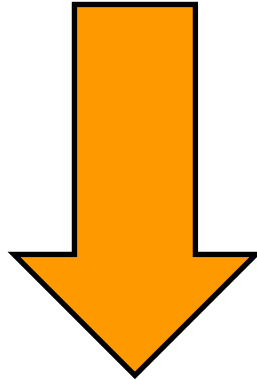
除簡報內所提供之歷史信息外，簡報事項係屬預測性陳述，受到風險及不確定性因素影響，可能造成實際結果與陳述內容發生不符，這些不確定性因素包括：技術平台是否順利導入利用、IP是否被客戶接受、客戶產品大量量產之能力及時間、產業及市場對半導體產品之供給及需求移轉、終端市場之穩定性及其他風險等。

大綱

- 公司營運模式
- 第4季營運回顧
- 成長機會與未來展望
- 問題與回答

何謂 Logic Non-Volatile Memory

Embedded NVM = LOGIC + 10 Masks

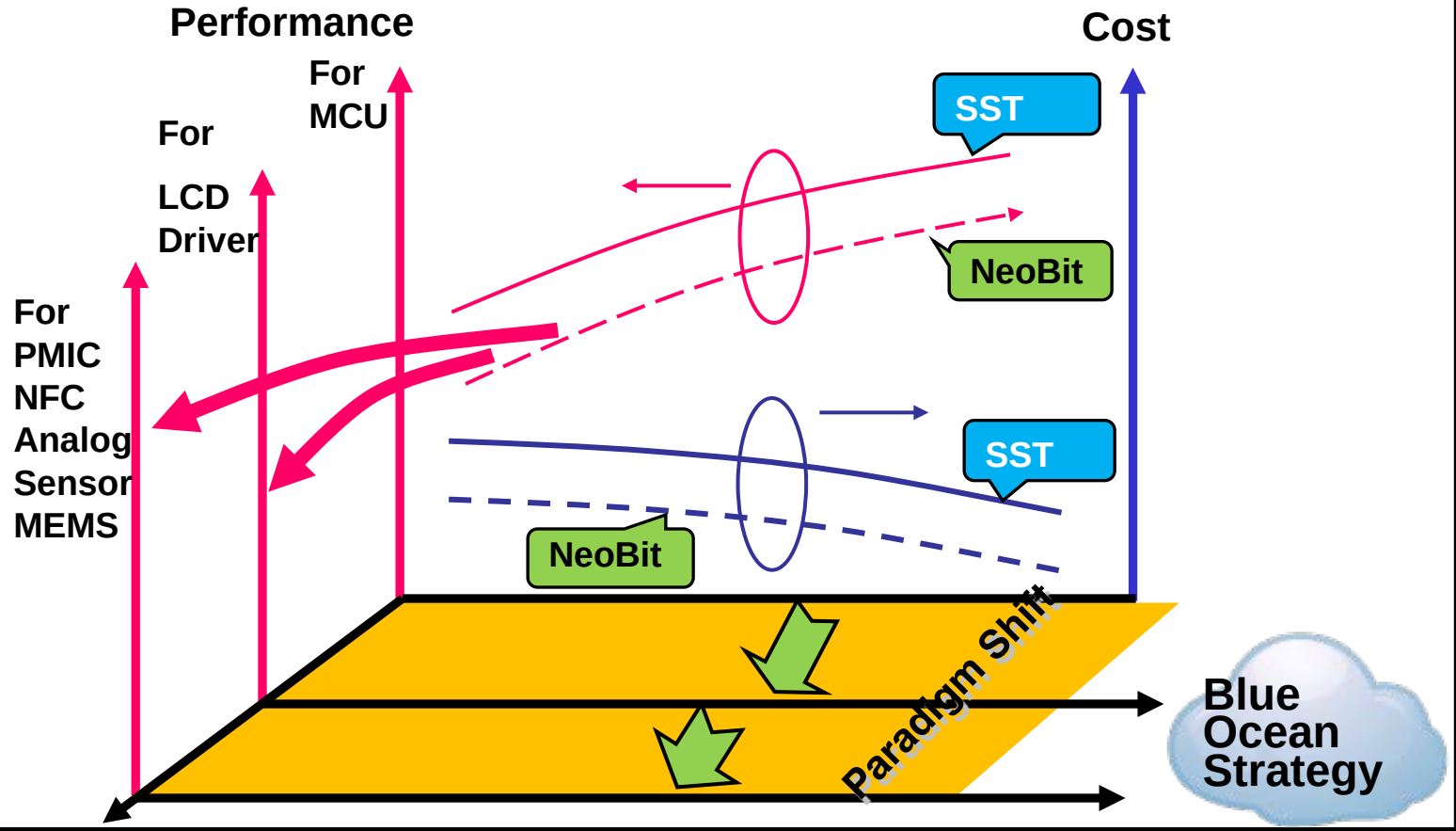


*30% more
cost reduction*

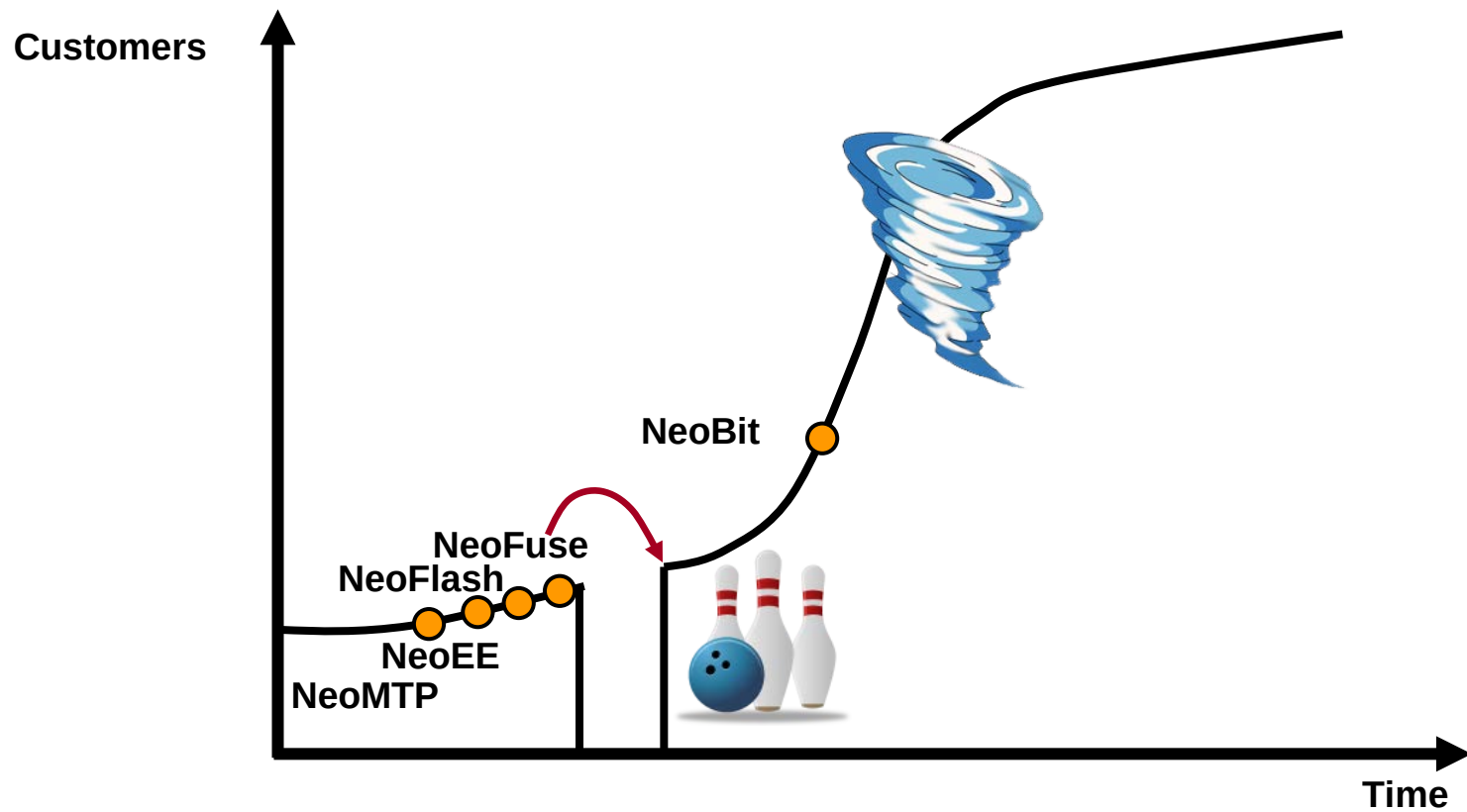
Embedded LOGIC NVM = LOGIC

破壞性創新

● *Innovation, Innovation, and Innovation !*

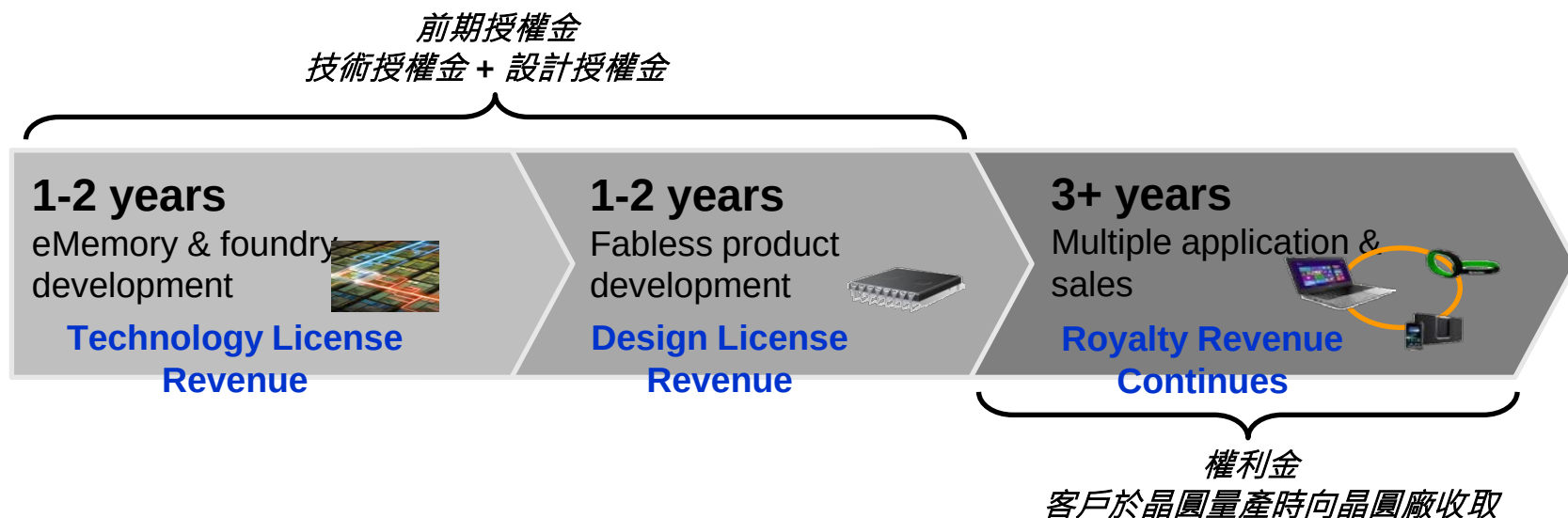


跨越鴻溝



營運模式

- 2000年成立，2002年第一個客戶，2005年開始獲利，2011年掛牌上市，掛牌以來，無市場籌資，無銀行借款，現金配股率超過100%。
- 全世界最大的logic non volatile Memory IP公司，217位員工，150位研發人員。
- 成長性指標: 1) 正在晶圓廠建構的製程平台數
2) 設計授權數
3) 權利金



全球客戶



	Taiwan	China	Korea	Japan	North America	Europe	Others
Foundry	5	6	3	2	1	0	1
IDM	0	0	0	8	2	1	0
Fabless	226	331	49	31	168	80	31

晶圓代工廠

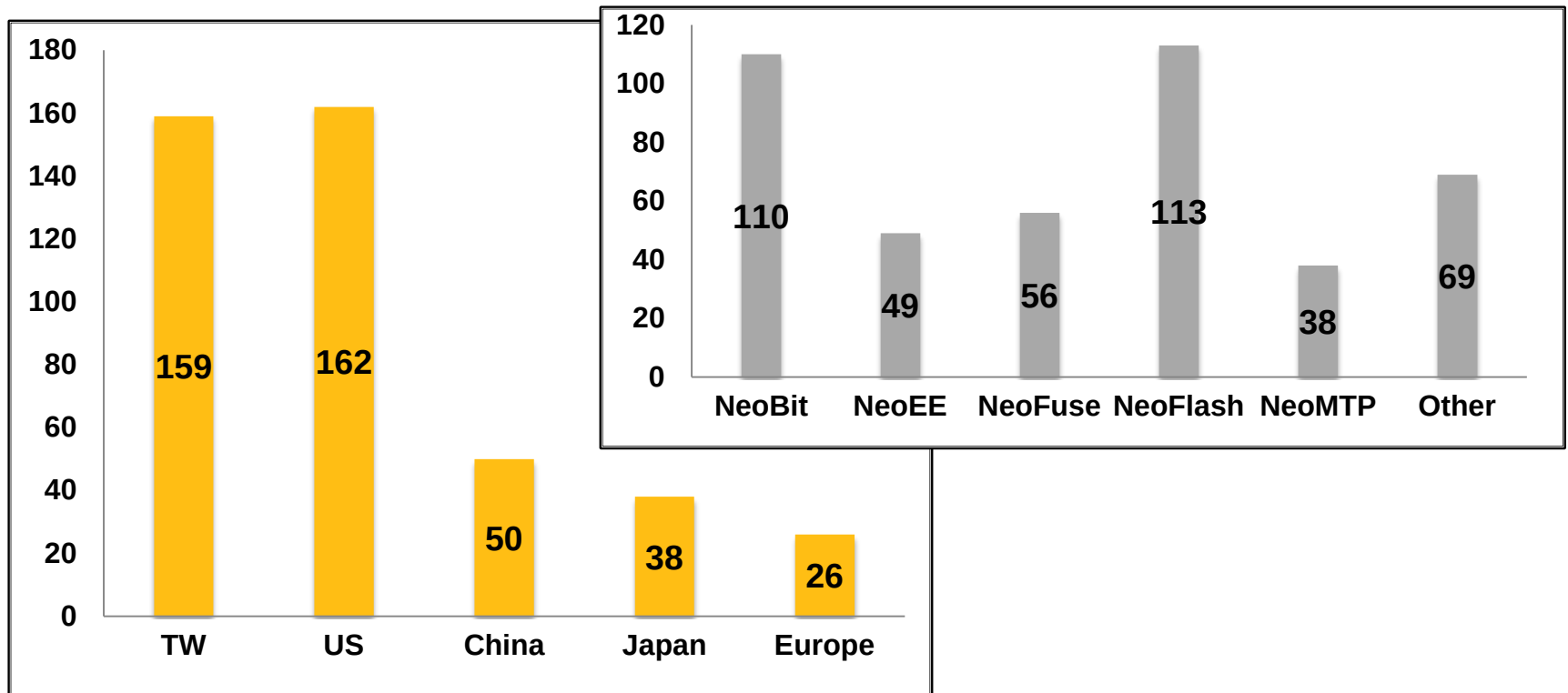


整合元件廠



專利佈局

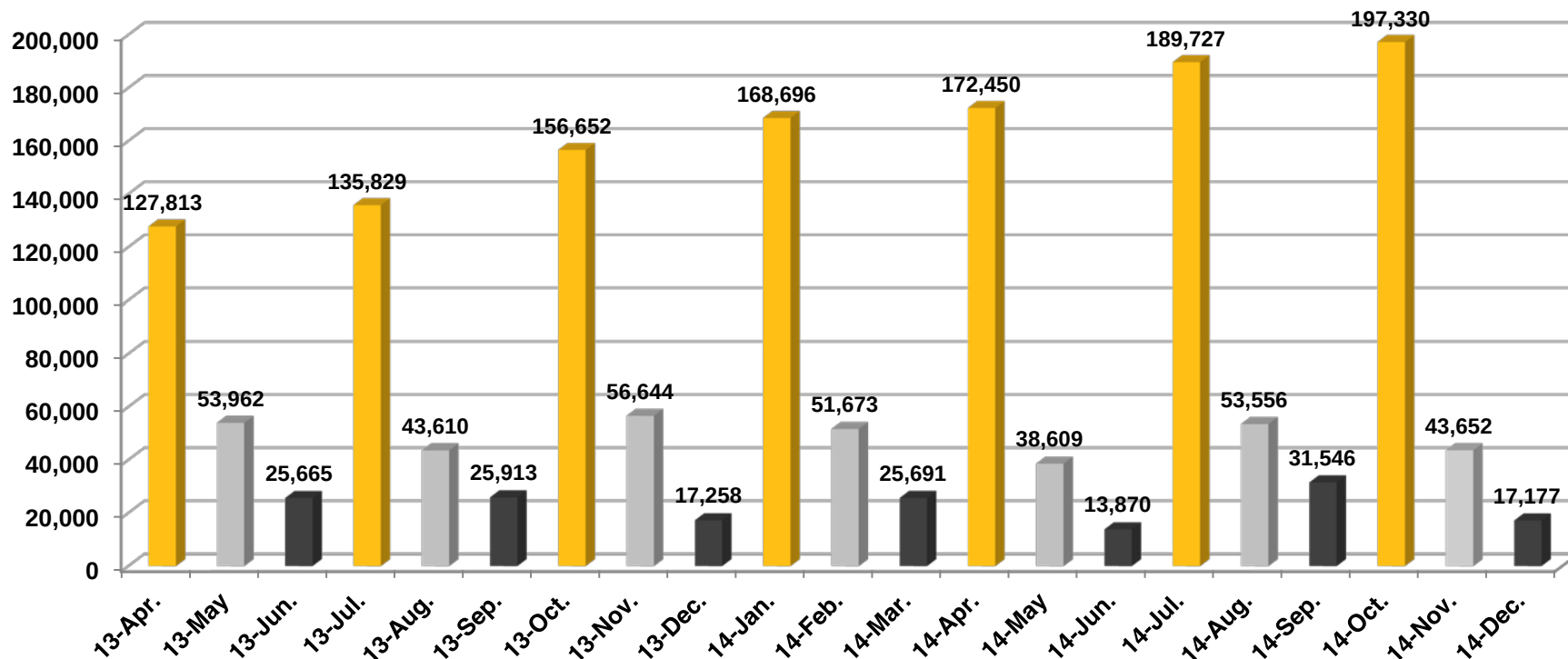
	3Q14	4Q14	Diff.
Pending	160	166	+6
Issued	255	269	+14
Total	415	435	+20



每季營收模式

- 每季第一個月認列絕大部份晶圓廠前一季使用本公司IP的出貨晶圓權利金及當月產生的技術授權金及設計授權金；第二個月則是少部份晶圓廠權利金及當月授權金；第三個月則無權利金收入，只有授權金。

Unit : NTD thousands



大綱

- 公司營運模式
- 第4季營運回顧
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第4季各項營收

單位：新台幣仟元

	4Q14	3Q14	QoQ Growth Rate	4Q13	YoY Growth Rate	1Q-4Q14	1Q-4Q13	YoY Growth Rate
授權費	51,849	61,981	-16.35%	56,933	-8.93%	246,073	245,688	0.16%
權利金	206,310	212,848	-3.07%	173,621	18.83%	757,904	562,570	34.72%
合計	258,159	274,829	-6.07%	230,554	11.97%	1,003,977	808,258	24.21%

單位：合約數

		4Q14	3Q14	1Q-4Q14	1Q-4Q13
技術授權數		3	6	21	19
設計 授權數	NRE	15	22	82	51
	使用費	99	88	363	342

綜合損益表

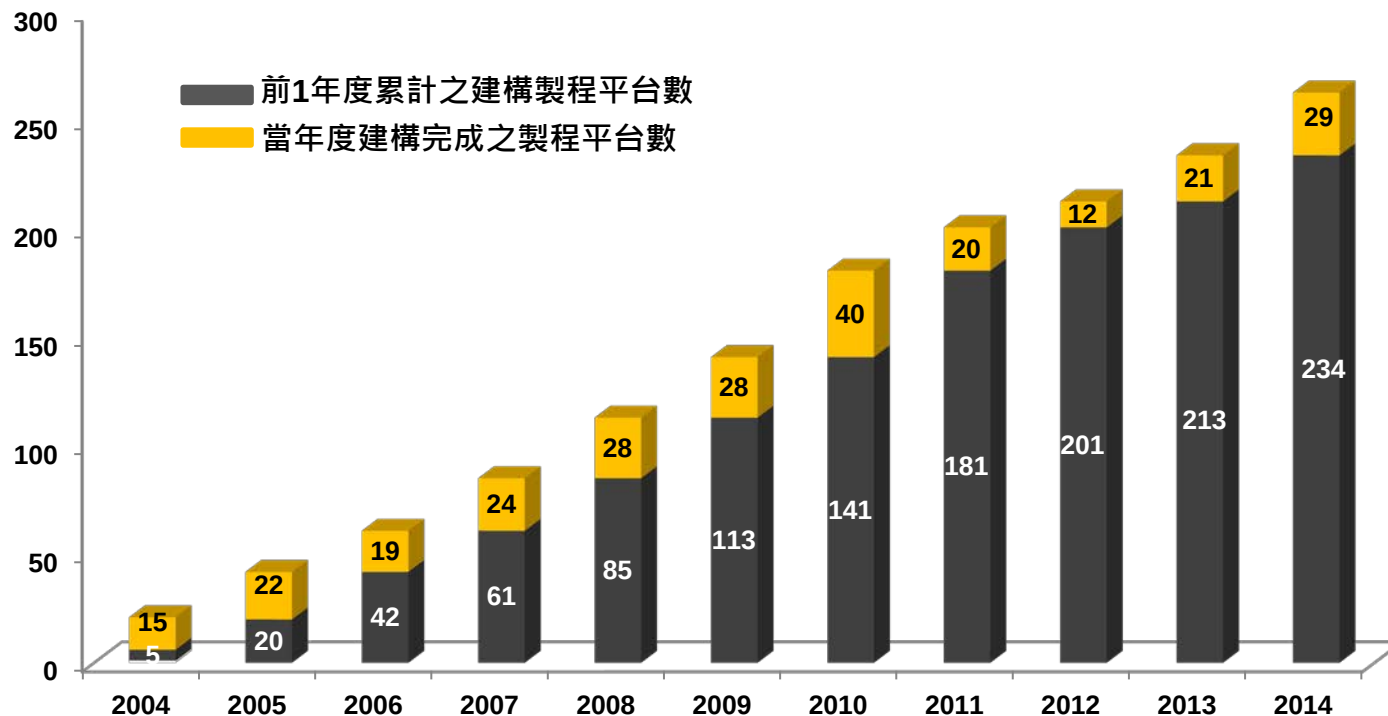
(單位:新台幣仟元)	3Q14	1Q-3Q14	1Q-3Q13	YoY
營業收入淨額	274,829	745,818	577,704	29.1%
營業毛利率	100%	100%	100%	-
營業費用	135,695	391,820	343,813	14.0%
營業淨利率	50.6%	47.5%	40.5%	+7.0ppts
營業外收入及支出	1,852	6,943	654	961.6%
本期淨利	124,352	317,673	199,372	59.3%
純益率	45.2%	42.6%	34.5%	+8.1ppts
每股盈餘 (單位: 新台幣元)	1.64	4.19	2.66	57.5%
權益報酬率	29.7%	25.3%	17.2%	+8.1ppts

技術授權合約及累計平台數

單位：合約數

年度	2012	2013	2014
授權數	12	19	21

註：與晶圓廠簽訂的技術授權合約所含的技術製程及授權金視合約內容而定，無特別季節性因素。



目前正在建構的技術製程平台

- Total (As of 4Q14) : **83**
- **32** for NeoBit, **28** for NeoFuse, **2** for NeoFlash, **14** for NeoEE, and **7** for NeoMTP.

	16nm	28nm	40nm	55/65nm	80/90nm	0.11~ 0.13um	0.15~ 0.18um	>0.25 um	Total
NeoBit	-	-	-	1	1	11	17	2	32
NeoFuse	1	7	4	9	1	4	2	-	28
NeoFlash	-	-	-	1	-	1	-	-	2
NeoEE	-	-	2	-	1	4	6	1	14
NeoMTP	-	-	-	1	2	2	2	-	7

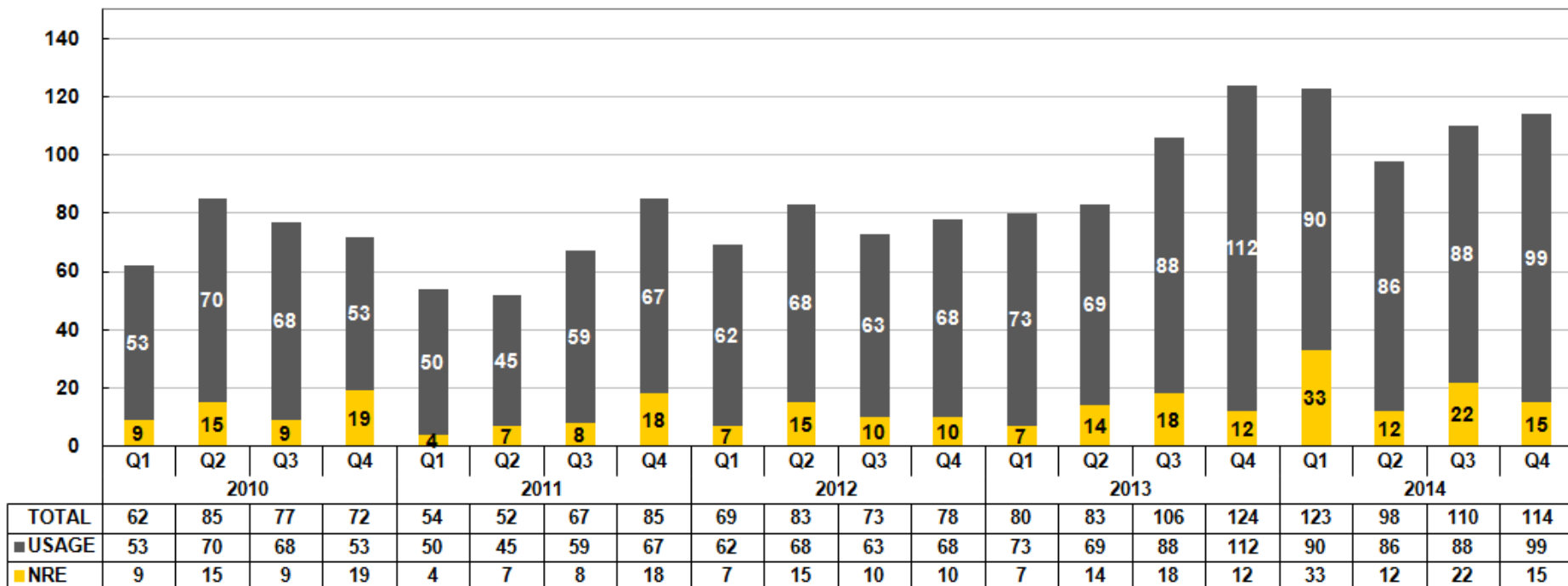
目前正在建構的技術製程平台

12" Fabs	Production	Development	NVM Type	Process Type
16nm	0	1	OTP	FF+
28nm	2	7	OTP	LP/HPM, HLP/HPM, LPS
40nm	1	6	OTP, MTP	HV-DDI, LP
55/65nm	7	12	OTP, MTP, Flash	LP, HV-DDI, HV-OLED, DRAM, CIS
80/90nm	4	5	OTP, MTP	HV-DDI, HV-OLED, LP
0.13/0.11um	3	6	OTP, Flash	HV-DDI, BCD, Generic
0.18um	0	1	OTP	BCD

8" Fabs	Development	NVM Type	Process Type
0.13/0.11um	16	OTP, MTP, Flash	HV-DDI, BCD, LP, RF, CIS, LL
0.18/0.16/0.152um	26	OTP, MTP	Generic, LP, LL, MR, HV, Green, BCD
0.25um	2	OTP, MTP	BCD
0.35um	1	OTP	UHV

每季設計授權數(New Tape Out)

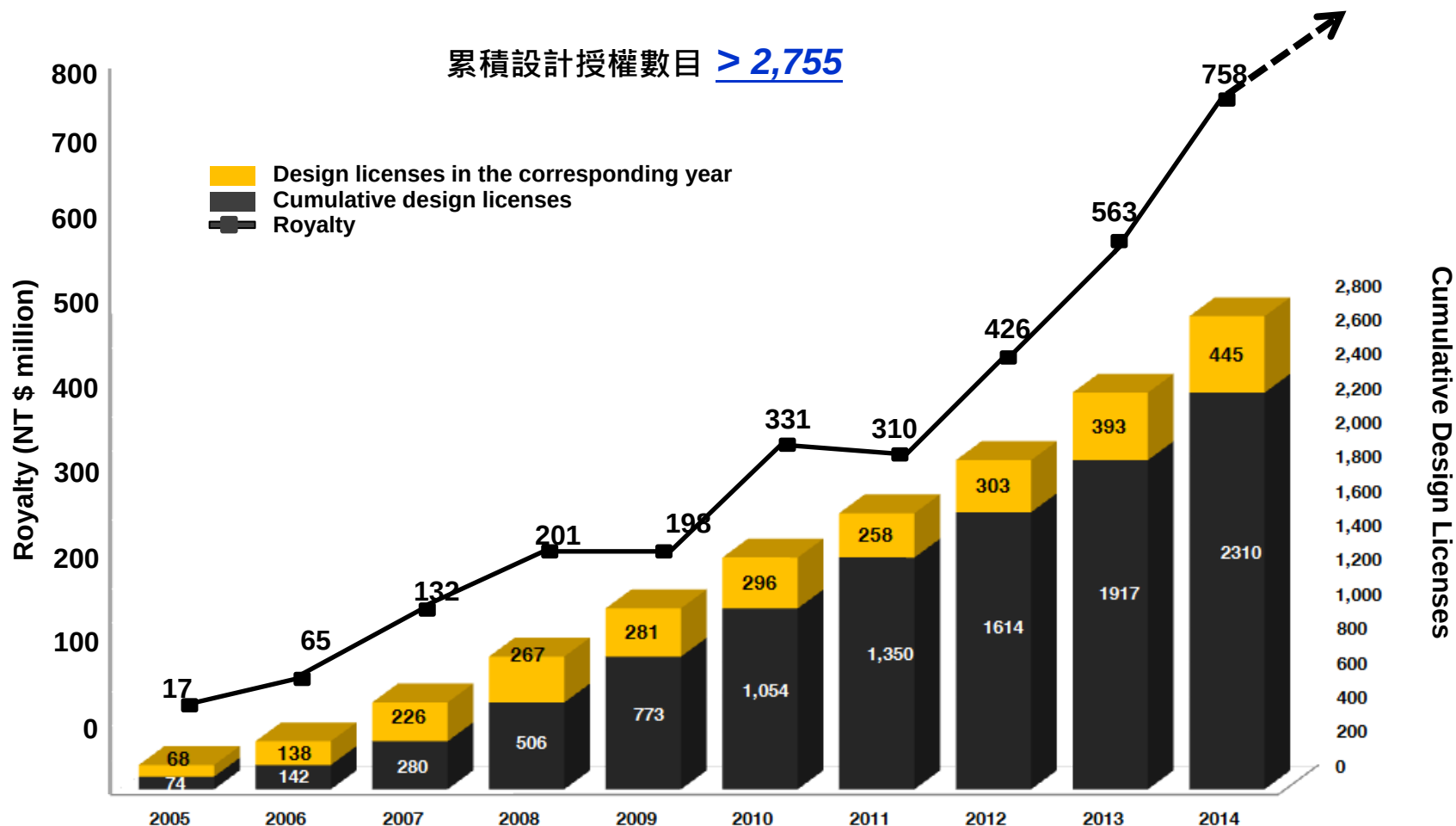
- Total **445** NTO as of 4Q 2014 (**393**@2013, **303**@2012, **258**@2011)



Usage：使用架上已認證及有量產經驗的IP，按年費(不計使用次數)或單次使用收費，設計導入到量產權利金貢獻時間約1年以內。

NRE：客製化的IP，需重新認證，導入到權利金貢獻約1-1.5年

權利金取決於過去累積的設計授權數目

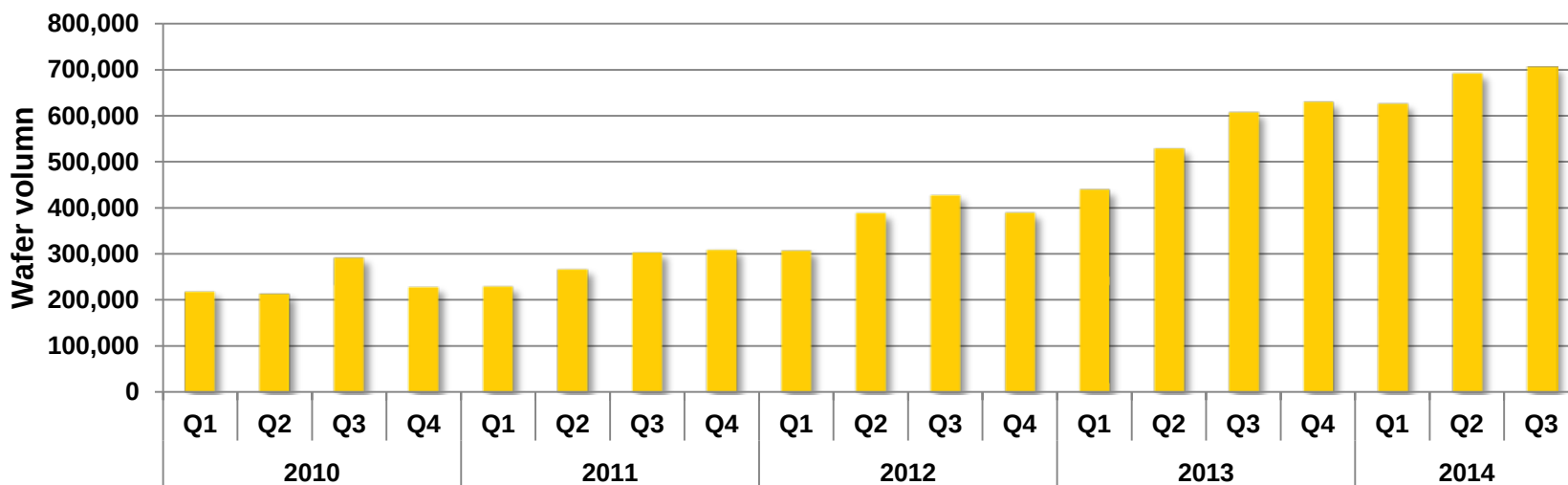


註1. 2009年因整體半導體景氣衰退，權利金年衰退1.5%。

註2. 2011年因2010年單一客戶預付權利金，導致2010年成長67%，2011年年衰退6.3%。

註3. 2009-2013年權利金年複合成長率為 30%。

每季量產晶圓片數



embedded eMemory IP in T Company (\$revenue); * % of Process node in T company total revenue in 3Q14

	Process node	*% of T	3Q14	2Q14	1Q-3Q14	1Q-3Q13
8"	0.5+	1%	0%	0%	0%	0%
	0.25/0.35	4%	33.5%	34.2%	30.5%	25.6%
	0.15/0.18	13%	13%	13.3%	13.3%	9.4%
	0.11/0.13	3%	21%	20.4%	20.8%	20.2%
12"	90nm	6%	16.4%	18.3%	16.3%	3.9%
	65nm	13%	0%	0%	0%	0%
	40/45nm	17%	0%	0%	0%	0%
	28nm	34%	0%	0%	0%	0%
	20nm	9%	0%	0%	0%	0%
8"		21%	16.5%	17%	16.1%	13.2%
12"		79%	1.4%	1.6%	1.4%	0.65%
Total		100%	4.5%	5.1%	4.5%	3.7%

大綱

- 公司營運模式
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力旺IP的應用

12"

8"

16/20nm

28nm

40nm

55/65nm

80/90nm

110/130nm

160/180nm

250nm

350nm

NeoBit

NeoFuse

NeoFlash

NeoEE

NeoMTP



力旺之 NVM 技術

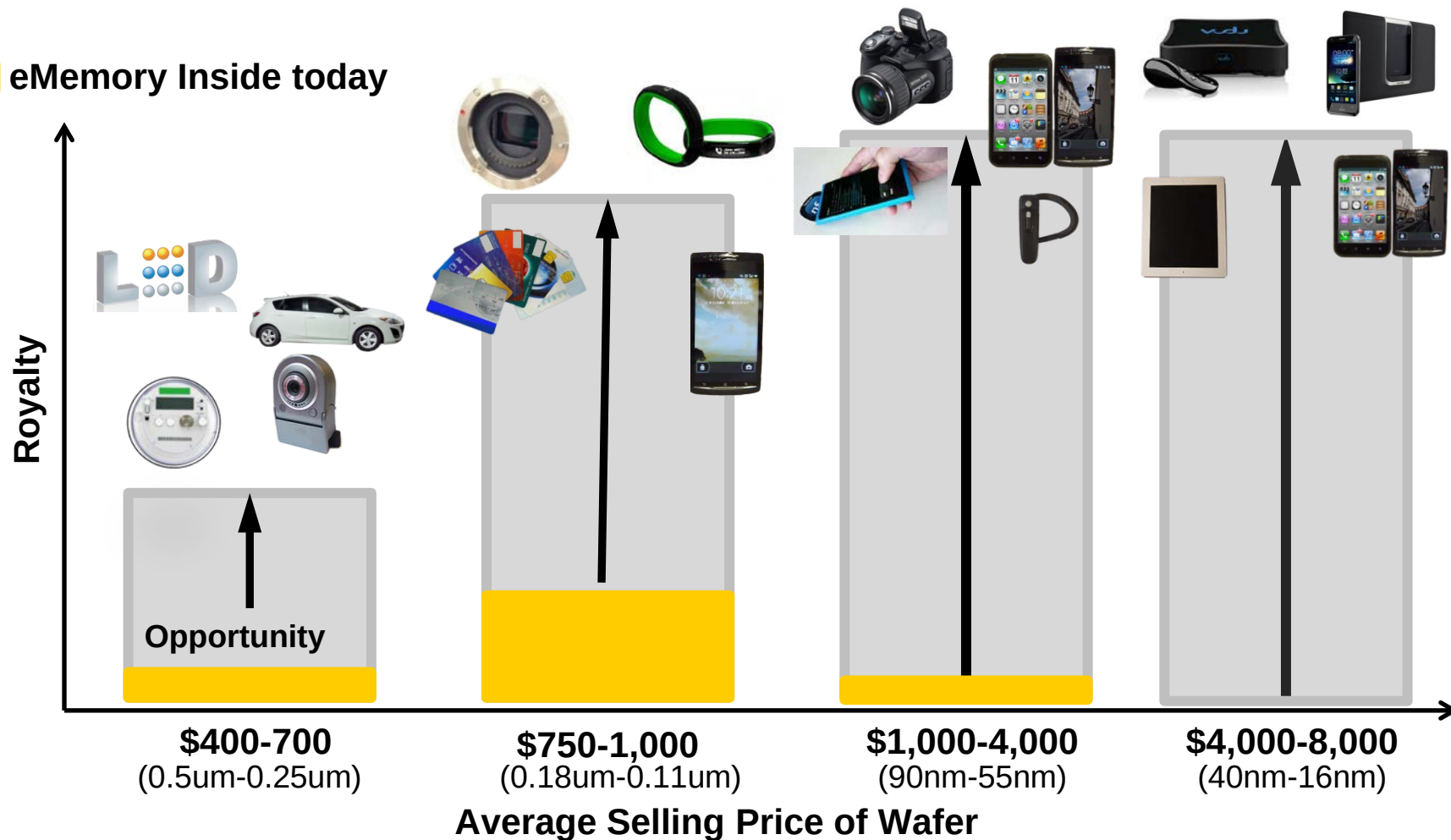
- **Logic NVM portfolio offers one-stop-shop solution.**

- › Compatible to any process
- › Robust structure
- › Low process cost
- › Competitive macro sizes
- › Easy integration
- › Easy porting

eMemory's NVM Technology	OTP			MTP	
	NeoBit	NeoFuse	NeoFlash	NeoEE	NeoMTP
Product Type	OTP	OTP	Flash	EEPROM	MTP
Endurance (Cycles)	10	10	1K~10K	10K~100K	1K~10K
Additional Mask Steps	0	0	2-3	0	0
Technology	Floating gate	Anti-Fuse	SONOS	Floating gate	Floating gate
Scalability	Simple	Simple	Simple	Simple	Simple
Memory Density	HD < 512Kb GHD < 16Mb	< 4Mb	< 2Mb	< 4Kb	< 512Kb

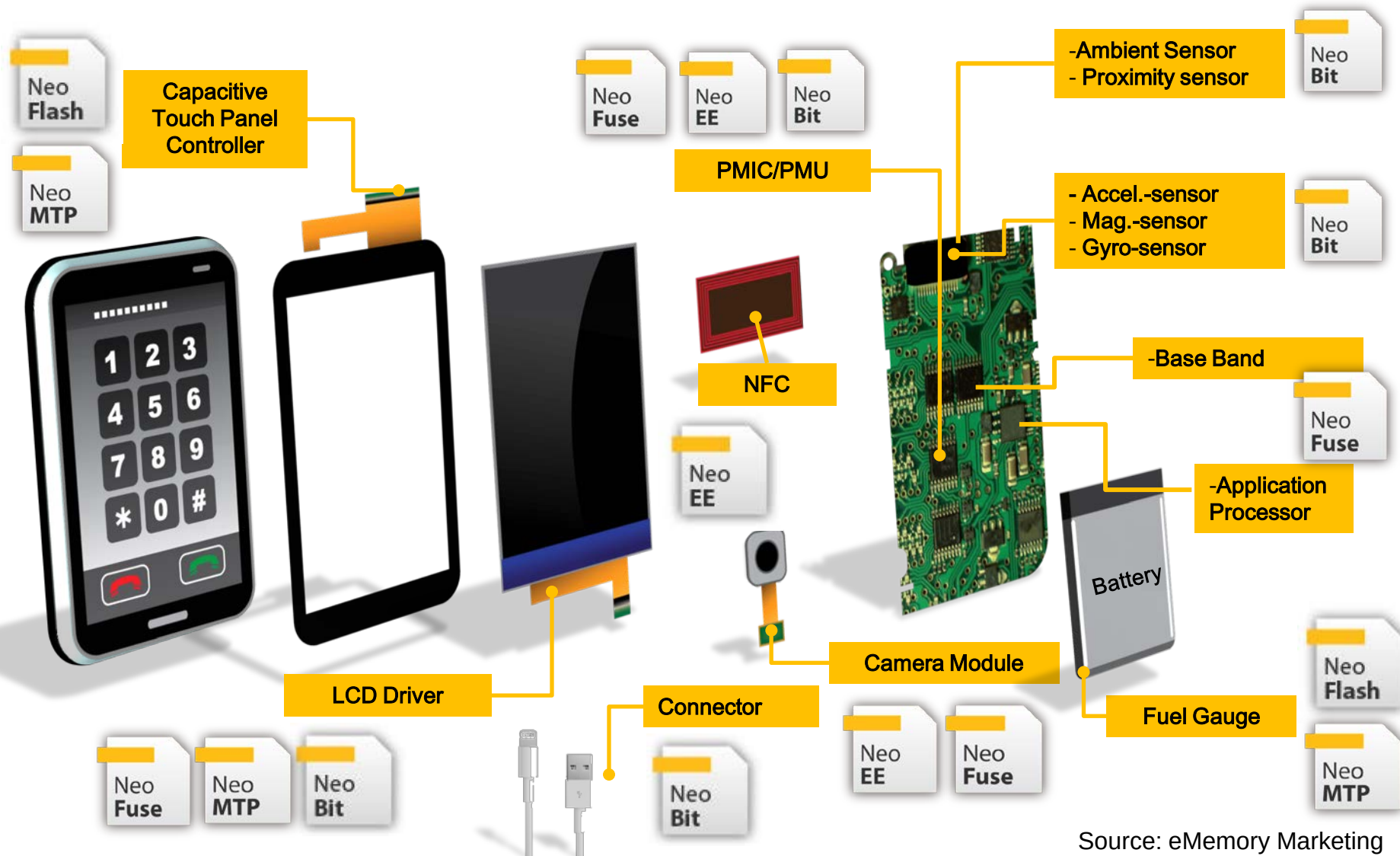
商機與潛力

 eMemory Inside today



Note: 2.2 million 8" equivalent wafers with eMemory IP were shipped in 2013. (~5% of WW foundry shipment)

智慧型手機中力旺 IP 的應用



Source: eMemory Marketing

3Q14 按IC種類之晶圓需求

IC Type	Equ. to 8-inch wafer (K)
AP	4964
PMU	4756
CIS sensor	4226
Fingerprint	4000
Smart card controller	3000
Base Band	2935
LCD driver (with TCON)	2013
Gauge IC	627
Touch panel controller (C)	556
Connectivity	395
STB controller	335
TV controller	327
Wifi controller	245
LED driver	243
DC-DC/AC-DC	176
Accelerator sensor controller	124
Light snesor	121
Bluetooth controller	121
Gyroscope sensor controller	104
TAG IC	76
DVD controller	67
MCU (8bits, LV/3.3V)	56
MCU (8bits, LV/3.3V)	56
P-Gamma	52
MCU (8bits, pure 5V)	51
NB CAM controller	42
Pressure sensor controller	20
Touch pad controller	18
PC CAM controller	15
Touch panel controller (R)	5
TCON (w/o driver)	4

2014.8.29 updated

先進液晶顯示驅動晶片

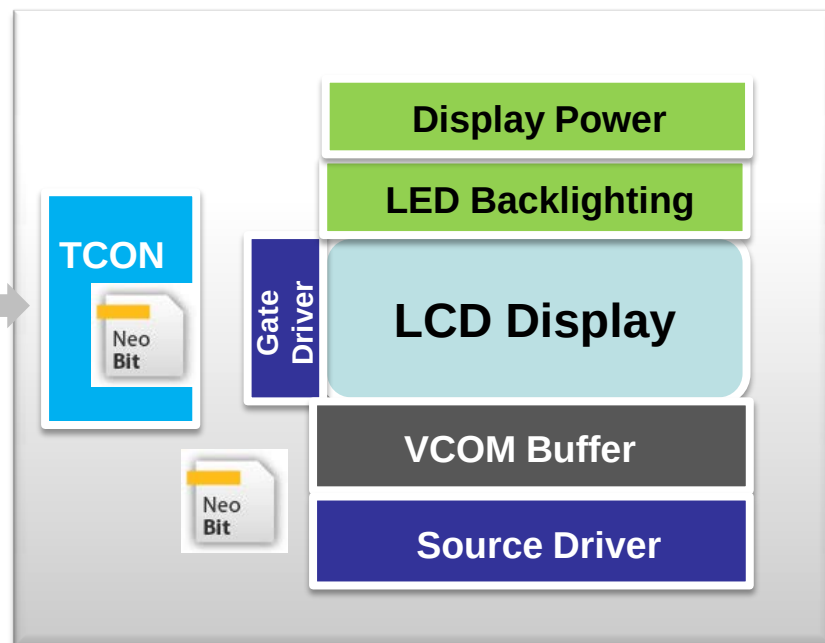
Process Technology : 0.11um HV/80nm HV/55nm HV



I/F
(LVDS, MIPI,...)



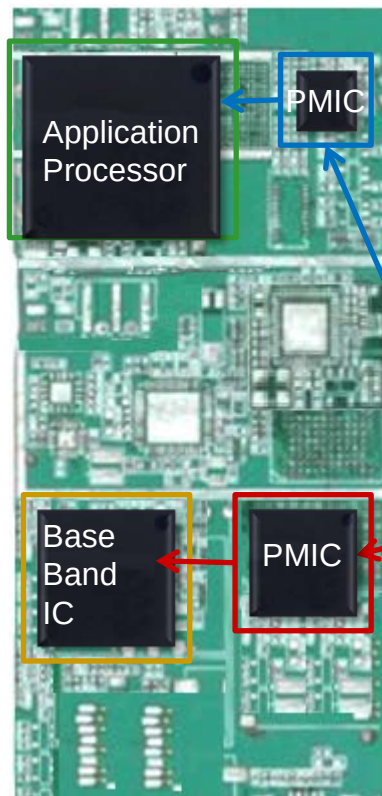
DDI



Density	Endurance	NVM Type	Purpose	NVM Usage
2K8~4K8	1	OTP	Trimming	1. Accuracy enhancement 2. Mismatch cancellation
			Code Storage	1. Gamma Correction Table 2. Timing Control Pattern 3. Color Engine Enhancement

應用於Baseband與Application Processor的電源管理晶片

Process Technology : Advanced 0.25um BCD/ 0.18um BCD/ 0.13um BCD
Mature 0.18um/0.16um/0.152um Logic

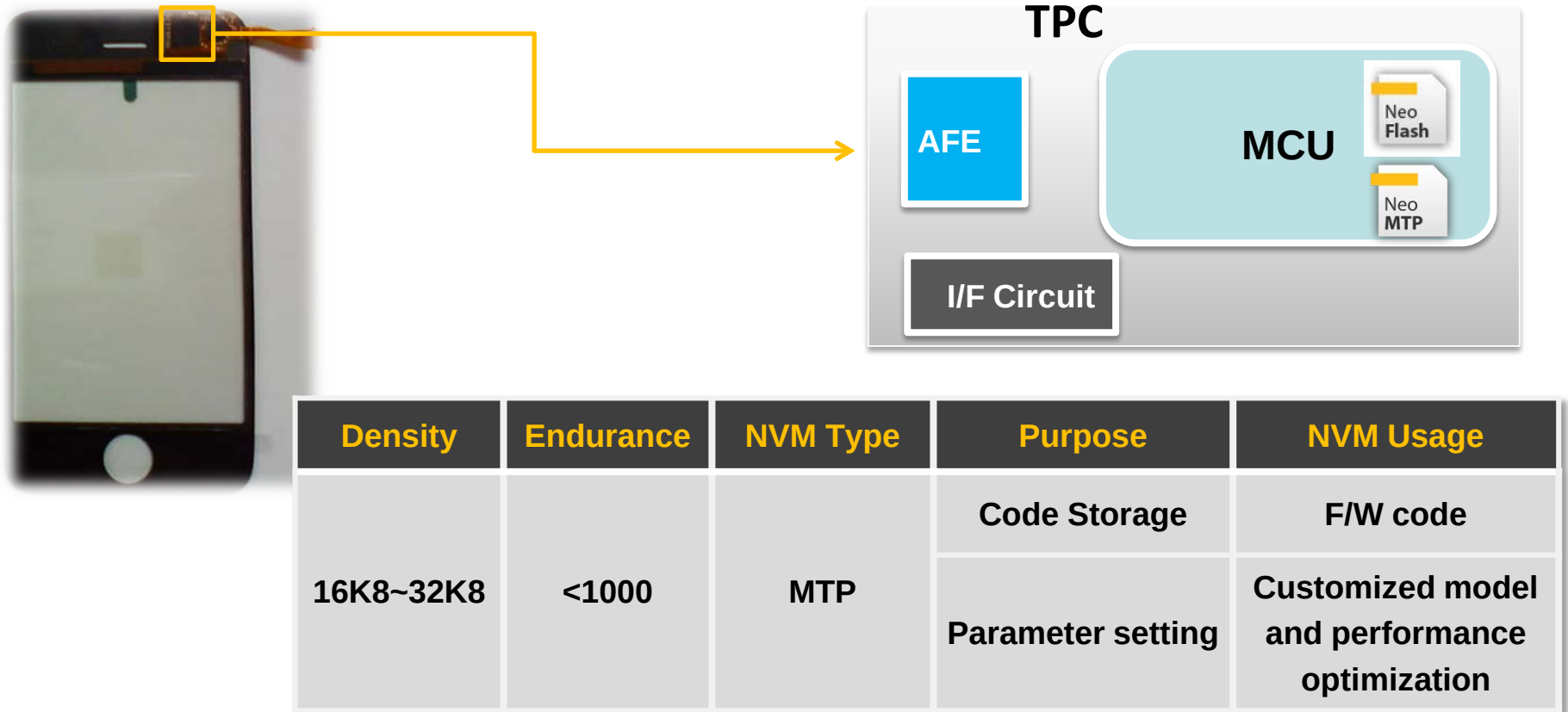


Density	NVM Type	Purpose	NVM Usage
2Kb~4Kb	OTP	Trimming	DC/DC, Bandgap
		Parameter Setting	Design flexibility & Performance optimization
		Code Storage	Start-up behavior & smart power saving algorithm



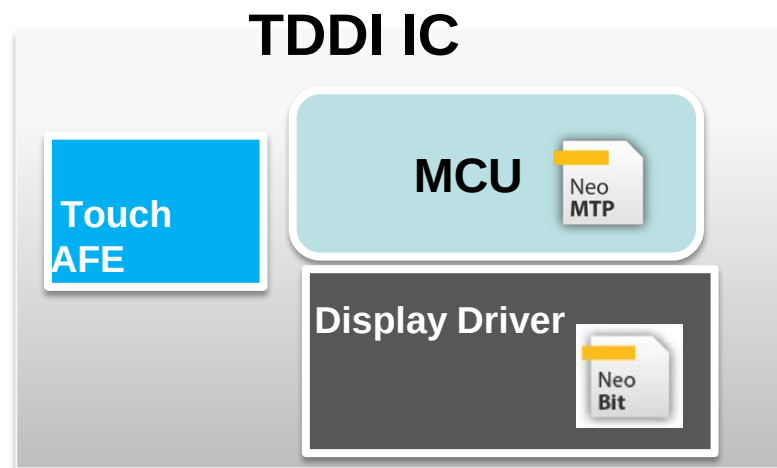
觸控面板控制器晶片

Process Technology : 0.16um HV/0.11um G



內嵌式觸控面板控制器晶片

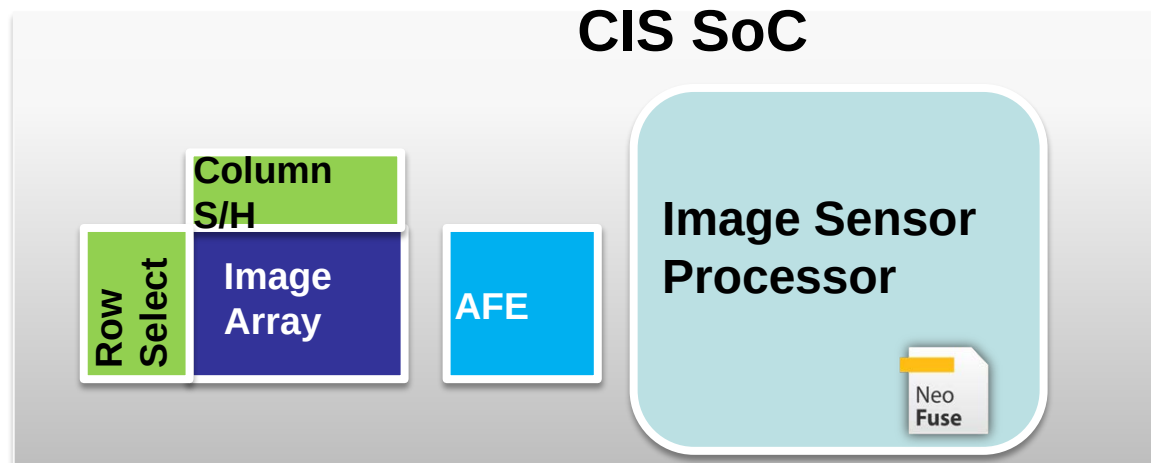
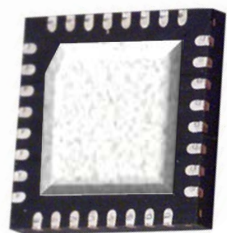
Process Technology : 0.11um HV/80nm HV/55nm HV



Density	Endurance	NVM Type	Purpose	NVM Usage
2K8~4K8	1	OTP	Trimming	Accuracy
			Code Storage	Gamma Table
16K8~32K8	<1000	MTP	Code Storage	Touch F/W Code
			Parameter setting	Performance Optimization

CMOS 影像感測器

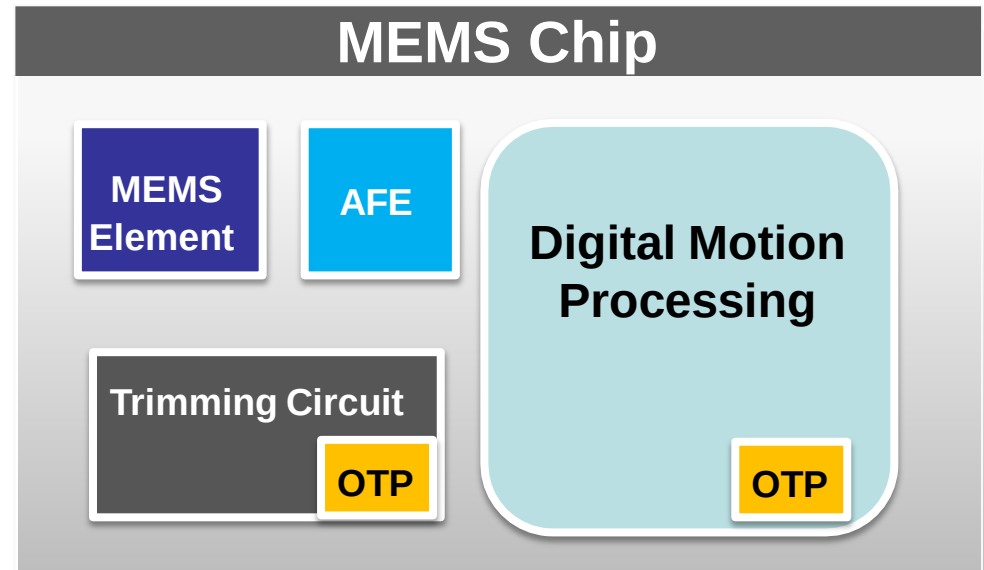
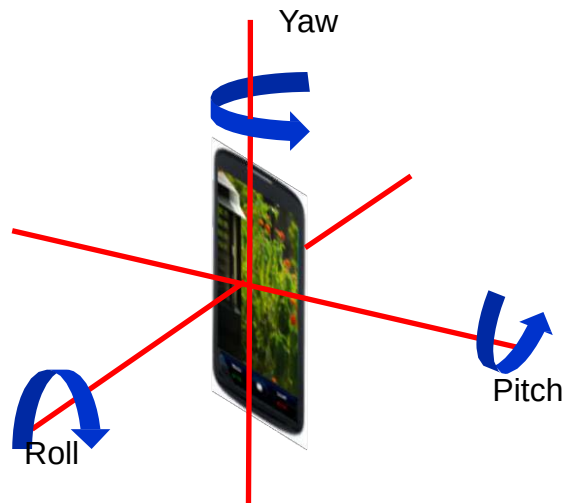
Process Technology : 0.11um CIS/90nm CIS/65nm CIS



Density	Endurance	NVM Type	Purpose	NVM Usage
2Kb~4Kb	1	OTP	Identification Setting	Product code
			Parameter Setting	Start-up initial setting
32K8	1	OTP/ROM	Code Storage	Boot load

微機電系統(MEMS)

180/160/15x nm HV/Logic for MEMS Controller



Density	NVM Type	Purpose	NVM Usage
2Kb~4Kb	OTP	Trimming	Factory trimming
		Parameter Setting	Signal filtering
		Code Storage	Geometric computation

資料防護

Authorized Product



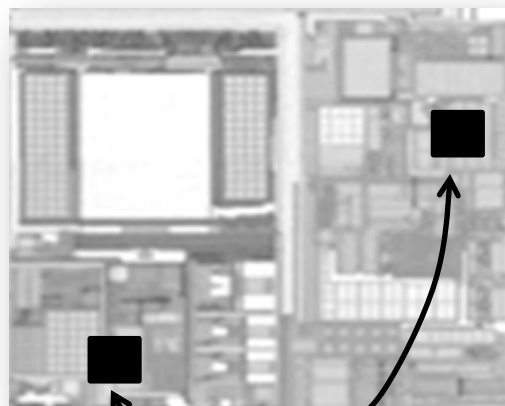
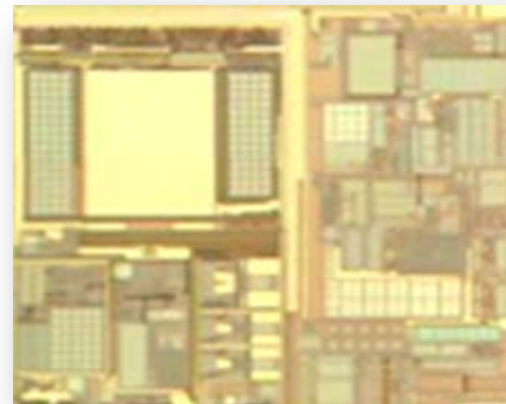
reverse
copy

re-produce



without protection

Fake Product

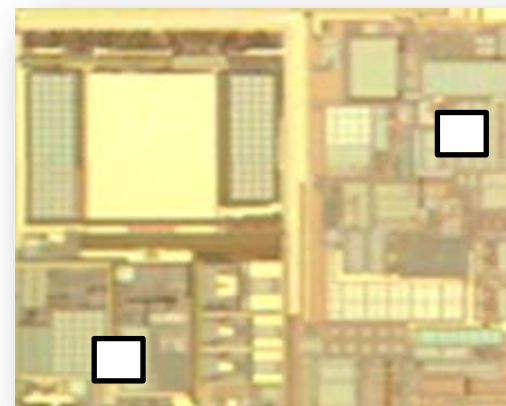


reverse
copy

re-produce



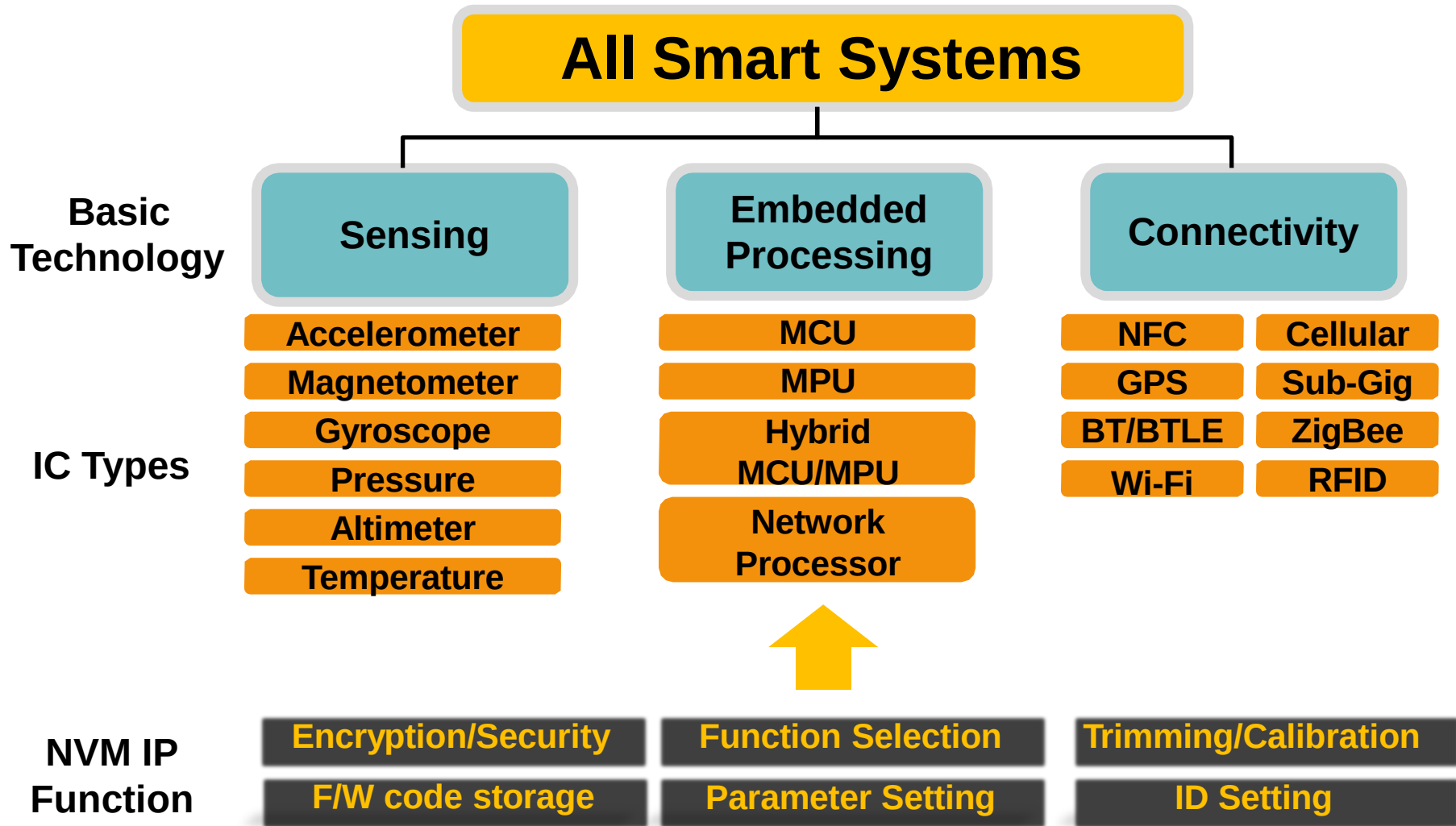
with protection



**Security IP/Code by
Authorized Use**

**Can NOT Work w/o
Security IP/Code**

在IoT中之NVM IP 需求



取代嵌入式快閃記憶體之競爭優勢

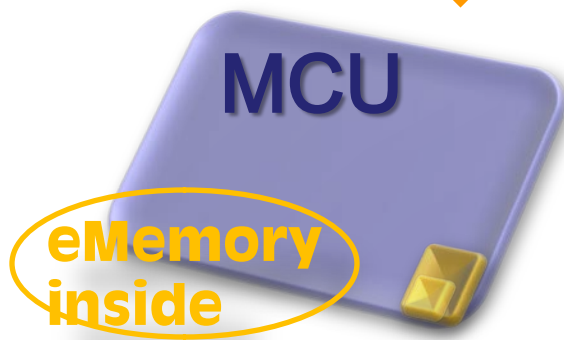


product design & manufacturing by
embedded Flash
Logic Process + 10 Masks



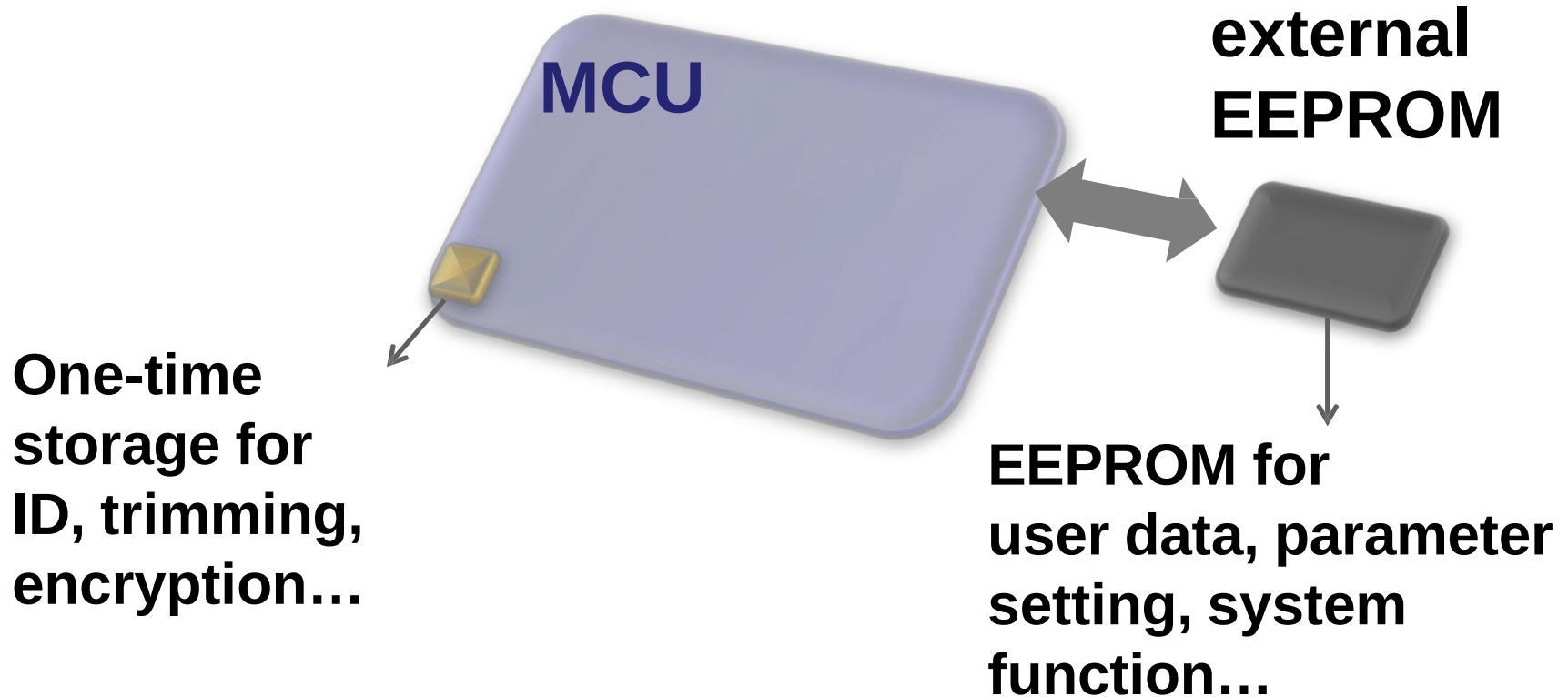
***30% more
cost reduction***

***wafer cost &
testing time***



product design & manufacturing by
Embedded Logic NVM (OTP/MTP)
Logic Process

EEPROM應用於MCU



NeoBit + NeoEE

Hybrid NVM solution (NeoBit + NeoEE) with customized SPEC & optimized size



- **One single IP by integration of NeoBit & NeoEE**
- **Help for system size reduction**

成長驅動力

Growth in value per mobile devices

- More chip applications per smartphone/tablet product .

Growth into more markets

- From consumer electronics and mobile devices to wearable devices.
- Adding new NVM product lines further enable more product applications.

Growth in more advanced technology

- Higher royalty per wafer is contributed from more advanced technology nodes.

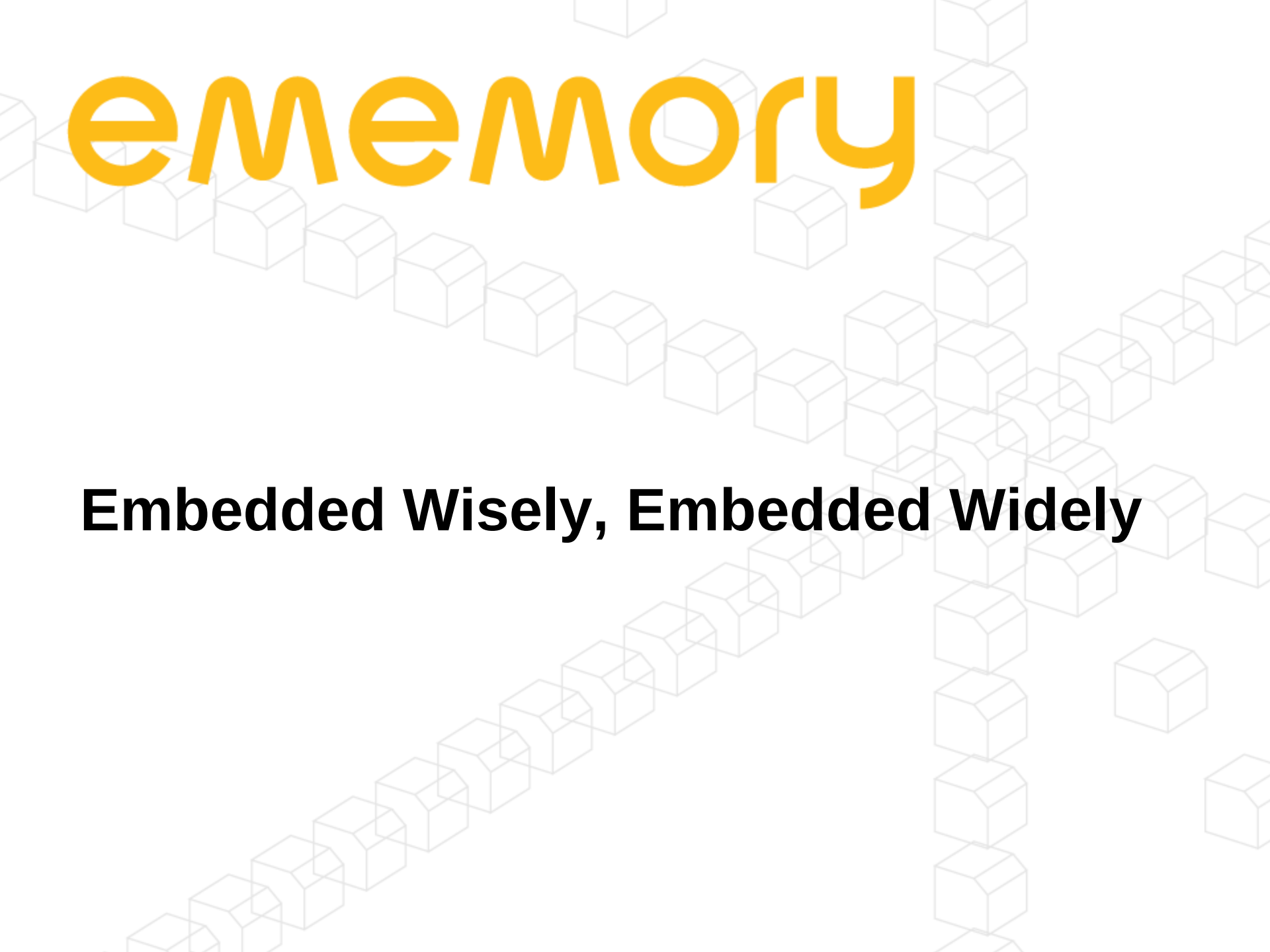
IoT great era

- Embedded Logic NVM will be a must.

未來展望

- 既有客戶晶片需求仍然強勁，新客戶晶片順利導入量產，營運成長動能可望持續。
- 隨著先進製程技術平台陸續建構完成，新設計案加速導入，來自先進製程的授權案與權利金貢獻可望明顯成長。
- 隨著行動裝置普及，帶動相關穿戴式與物聯網應用，對低功耗、低成本及安全考量需求增加，加速嵌入式非揮發性邏輯記憶體IP的導入設計應用。

Q & A



ememory

Embedded Wisely, Embedded Widely