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Q3 2020 Financial Results

The EPS of Q3 2020 was 2.26 NTD, ROE was 38.8%

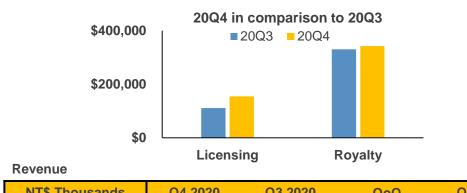
(thousands of NT dollars)

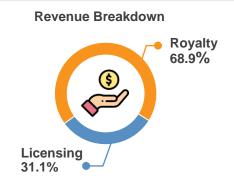
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	Q3 2020	Q2 2020	Change (QoQ)	Q3 2019	Change (YoY)	Q1-Q3 2020	Q1-Q3 2019	Change (YoY)
Revenue	441,259	423,276	4.2%	336,587	31.1%	1,279,971	1,048,189	22.1%
Gross Margin	100%	100%	-	100%	-	100%	100%	-
Operating Expenses	243,913	227,364	7.3%	197,399	23.6%	692,740	586,376	18.1%
Operating Income	197,346	195,912	0.7%	139,188	41.8%	587,231	461,813	27.2%
Operating Margin	44.7%	46.3%	-1.6 ppts	41.4%	3.3 ppts	45.9%	44.1%	1.8 ppts
Net Income	168,581	169,317	-0.4%	120,170	40.3%	514,656	412,419	24.8%
Net Margin	38.2%	40.0%	-1.8 ppts	35.7%	2.5 ppts	40.2%	39.3%	0.9 ppt
EPS (Unit: NTD)	2.26	2.28	-0.9%	1.62	39.5%	6.92	5.56	24.5%
ROE	38.8%	41.9%	-3.1 ppts	29.1%	9.7 ppts	39.5%	33.3%	6.2 ppts

Note: Revenue of Q3 2020 in terms of US\$ is US\$14.96 mil, up 5.7% QoQ, and up 38.2% YoY.

Revenue in Different Stream

Q4 revenue up 45.3% YoY in US dollar terms





NT\$ Thousands	Q4 2020	Q3 2020	QoQ	Q4 2019	YoY	Q1-Q4 2020	Q1-Q4 2019	YoY
Licensing	154,472	111,125	39.0%	115,944	33.2%	490,105	430,263	13.9%
Royalty	342,210	330,134	3.7%	245,952	39.1%	1,286,548	979,822	31.3%
Total	496,682	441,259	12.6%	361,896	37.2%	1,776,653	1,410,085	26.0%
	_							

US\$ Thousands	Q4 2020	Q3 2020	QoQ	Q4 2019	YoY	Q1-Q4 2020	Q1-Q4 2019	YoY
Licensing	5,350	3,765	42.1%	3,798	40.9%	16,610	13,944	19.1%
Royalty	11,859	11,195	5.9%	8,047	47.4%	43,586	31,810	37.0%
Total	17,209	14,960	15.0%	11,845	45.3%	60,196	45,754	31.6%

Revenue by Technology

The royalty of NeoFuse has a growth of 119.8% YoY in Q1-Q4

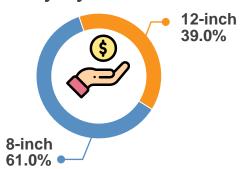
					Q4 2020					
	Т	otal Revenu	ıe	Lice	Licensing Revenue			Royalty Revenue		
Technology	% of Q4 Revenue	Change (QoQ)	Change (YoY)	% of Q4 Licensing	Change (QoQ)	Change (YoY)	% of Q4 Royalty	Change (QoQ)	Change (YoY)	
NeoBit	44.7%	-6.1%	12.2%	17.1%	6.2%	-17.1%	57.2%	-7.6%	17.8%	
NeoFuse	49.5%	40.0%	81.2%	71.3%	60.6%	67.5%	39.6%	26.7%	94.1%	
PUF-Based	0.5%	-58.7%	-40.1%	1.7%	-58.7%	-40.1%	0.0%	0.0%	0.0%	
MTP	5.3%	14.9%	9.1%	9.9%	33.6%	9.3%	3.2%	-3.9%	8.9%	

		Q1-Q4 2020							
	Total Revenue		Licensing	Revenue	Royalty Revenue				
Technology	% of Q1-Q4 Revenue	Change (YoY)	% of Q1-Q4 Licensing	Change (YoY)	% of Q1-Q4 Royalty	Change (YoY)			
NeoBit	50.8%	5.6%	19.5%	-17.4%	62.7%	9.2%			
NeoFuse	43.6%	74.7%	68.8%	38.0%	34.0%	119.8%			
PUF-Based	0.7%	148.2%	2.5%	148.2%	0.0%	0.0%			
MTP	4.9%	-18.1%	9.2%	-30.9%	3.3%	1.9%			

Royalty Revenue by Wafer Size

12-inch wafer increased 80.9% YoY in Q4

Q4 Royalty Breakdown



- √ 12-inch wafers contributed 39% of royalty, increased 1.7% sequentially and 80.9% YoY.
- √ 8-inch wafers contributed 61% of royalty, increased 5% sequentially and 21.2% YoY.

Royalty

W-6 0'		Q4 2020		Q1-G	Q1-Q4 2020		
Wafer Size	% of Q4	Change (QoQ)	Change (YoY)	% of Q1-Q4	Change (YoY)		
8-Inch	61.0%	5.0%	21.2%	62.3%	18.0%		
12-Inch	39.0%	1.7%	80.9%	37.7%	61.4%		



eMemory Embedded Everywhere

eMemory's IP seeks to penetrate across all the applications



✓ Product Applications:

eMemory's IP are already applied into different applications, which includes PMIC, LCD driver, Sensors, RFID, OLED Driver, Connectivity IC, DTV, STB, SSD Controller, Bluetooth, TDDI, MCU, Fingerprint Sensor, Smart Meters, Surveillance, ISP, CIS, DRAM, embedded Flash and FPGA.

✓ Future Target:

AP, GPU, CPU, Flash, IoT, AI, autonomous driving



✓ The Future in Security Chip IP:

The rapid growth in AloT and 5G drive the demand for hardware security. OTP and PUF are indispensable for root of trust in hardware security.

✓ PUF-based Security Solutions:

To satisfy the market needs, eMemory developed a new series of PUF-based security solution, including PUFrt, PUFiot, PUFse and PUFflash.

Our Perspectives

eMemory continue to create value for the industry and our shareholders

Licensing & Royalty



✓ Licensing:

 NeoFuse and NeoPUF will continue to grow due to increasing demand for design license activity and technology platforms that are continuously built among foundry partners.

✓ Royalty:

- 8" royalty will grow due to PMIC content increase in 5G smartphone and demand pick-up for automotive and IoT applications.
- 12" royalty will have a strong growth as customers are ramping up productions for ISP, OLED, Networkingrelated such as WiFi 6, Multimedia-related, DRAM, SSD controller and others.



New Application & Technology Development

✓ For New Business Development:

- NeoFuse in advanced process is adopted for secure key storage. We expect this will be a trend for security requirement.
- Business activities of PUF-based security solutions are in progress in applications of IoT, IIoT, AI, Blockchain, DPU, UFS, and automotives.
- Partnership with ARM and RISC-V will extend our PUF-based security platform.

✓ For New IP Technology Development:

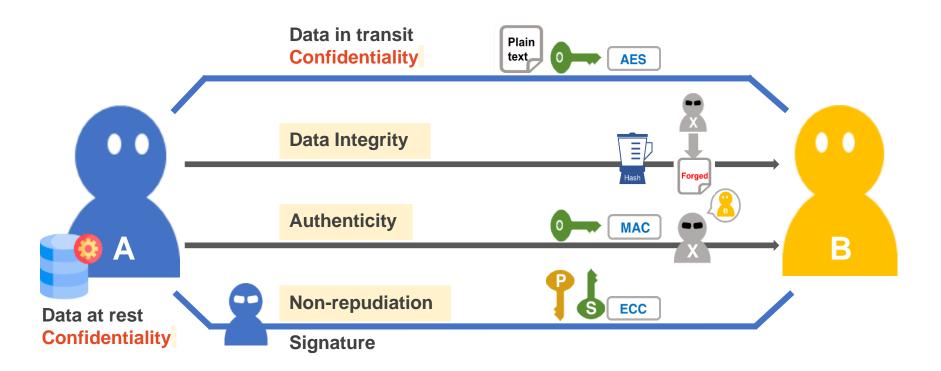
- Developing 6nm and 5nm plus (N5P) technology with leading foundry partners; demonstrated 6nm silicon results successfully.
- Announced crypto processor, PUFiot
- Develop PUF-based solution to be implemented in HSM.

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Typical AI Security Concerns We Face

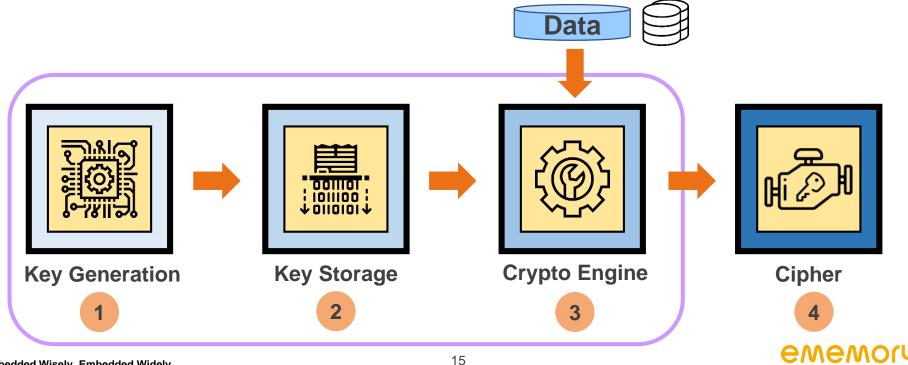
We utilize cryptographic operations and keys to ensure security



The Foundation of Al Security: RoT

We refer to the anchor of secure device as Root-of-Trust (RoT)

Protecting keys right from generation is therefore pivotal



Towards a RoT for AI Security

Generating key, storing key and using key in cryptographic operation

It all comes down to 3 essential problems to solve.



How can we generate secret key?



→ Chip Fingerprint



How can we store keys securely?



Static & Dynamic Entropy from Root-of-Trust



How can we secure key usage in operations?

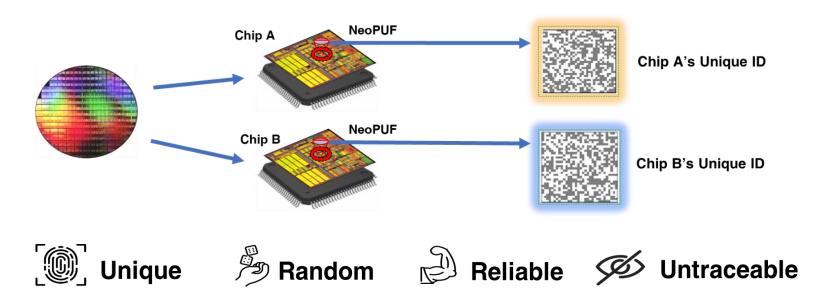


Equip Root-of-Trust with complete set of algorithms

NeoPUF: The Inborn Chip Fingerprint

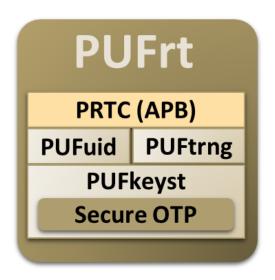
A truly random sequences for applications with high security requirements can be generated

 NeoPUF utilizes Quantum Tunneling mechanism to generate a one-andonly "biometric identifier" of each and every chip



PUFrt: A PUF-based Hardware RoT

PUFrt integrates altogether key generation, key storage, and entropies to protect operations



A Highly Integrated PUF-based HRoT with complete Anti-tampering Design



On-chip <u>UIDs</u> for Authentication



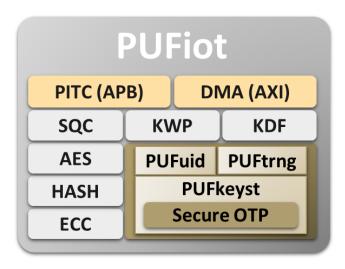
PUF-based TRNG for crypto usages



PUFiot: A PUF-based Security Crypto Processor

PUFiot incorporates crypto-algorithms, enabling all security functions

A HW Crypto processor with entropy protection on data, keys and crypto operations





- Key Generation
- Key Wrapping



- Integrity Check
- Encryption



 Symm. & Asymm. Authentication



- Secure Boot
- TLS
- · Key mgmt.



PUF-based Solutions Secure All Al Operations

PUFrt and PUFiot enhance security functions during AI operations

Security Al Operation	Threat Model	Countermeasure	Security Function	Security Solution
Device Boot & Authentication	Malicious FW loadedUnauthorized device	Secure BootingAuthentication and Provisioning	Secure key storageUnique Identifier (UID)	PUFrt PUF-based Root of Trust
Model Training & Deployment	Training Data stolenModel stolen from edgeData/Model Modified	 Protect data-in-transit Protect deployed model Integrity Check	Data/Model Encryption Hashing and Signature	PUFiot PUF-based Crypto Processor
Input & Inference	Unauthorized userUser data privacy leakInput/Result tampered	 Authentication and Provisioning Protect data-at-rest Ensure integrity/source 	 Unique Identifier (UID) Data Encryption Hashing and Signature	PUFiot PUF-based Crypto Processor

Summary: PUF-based Solutions for Al Security

The underlying benefit of using a PUF in cryptography is its "uniqueness" and "unpredictability"



NeoPUF-based Hardware Root of Trust, containing NeoPUF and OTP, provides



- Unique Identity
- Secure Key Storage
- True Random Number Generator
- Anti-Tampering Solutions



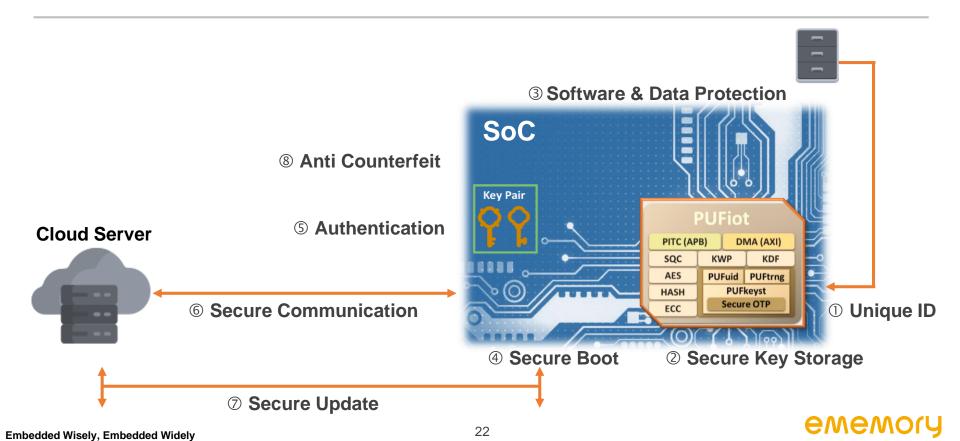
Plus

- High Manufacturability
- Ultra Low Cost



A High Value Proposition for Secure Al Applications

PUF-based Solutions Safeguard Products Throughout Entire Lifecycle





Q&A



Company Overview

eMemory is the global leader of embedded non-volatile memory IP



Founded

Based in Hsinchu, Taiwan. IPO in 2011. Over 31M wafers shipped.

800+ Patents Issued

263 pending patents. 286 employees with 67% R&D personnel.

Best IP Partner

TSMC Best IP Partner Award since 2010.

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Quarterly Revenue Pattern

eMemory's quarterly revenue pattern

- 1st month: Receive License Fees of the month and Royalty from most foundries on previous quarter's wafer shipments.
- 2nd month: Receive License Fees of the month and Royalty from other foundries.
- 3rd month: License Fees Only.

Note: One foundry pays royalty semiannually, reported in Jan and July revenue.



Worldwide Customers

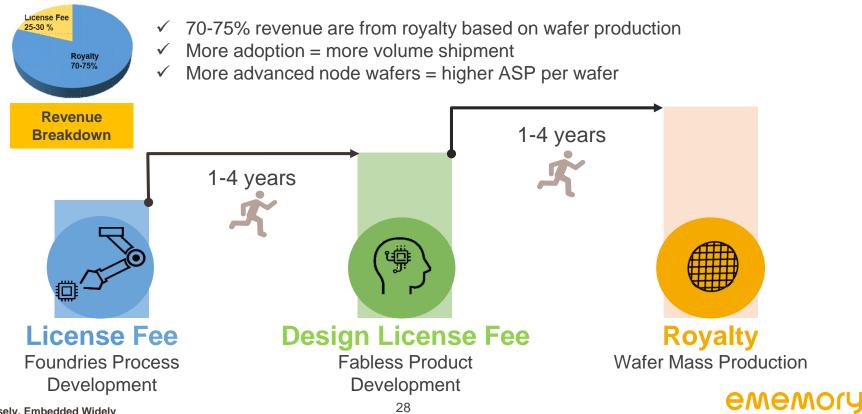
Our IP solutions are adopted by leading foundries, IDMs and fabless worldwide

Country	Foundry	IDM	Fabless
Taiwan	4	1	302
China	8	0	812
Korea	4	0	87
Japan	4	7	64
North America	1	1	306
Europe	2	1	177
Others	1	0	76



Business Model

Recurring royalty is the backbone of our business



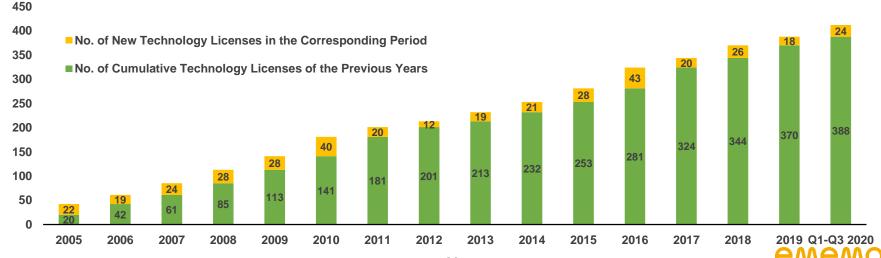
Technology Licenses

Cumulative technology licenses

Number of Licenses

Year	2016	2017	2018	2019	Q1-Q3 2020
License	43	20	26	18	24

Note: Terms (including number of process platforms and licensing fees) for each technology license are set contractually. Payments are made according to set milestones, and there are no particular seasonal factors involved.



New Technology Under Development

Products in different process nodes

- ✓ New technologies are being developed for 98 platforms by Q3 2020.
- ✓ 7 licensing contracts were signed, 6 for NeoFuse and 1 for NeoMTP.

Technology	5/6nm	7/10nm	12/16nm	22/28nm	40nm	55/65nm	80/90nm	0.11~ 0.13um	0.15~ 0.18um	>0.25um
NeoBit	-	-	-	-	-	1	2	10	10	1
NeoFuse	2	1	3	9	3	11	8	1	1	-
PUF-Based	2	-	2	2	2	1	-	-	-	-
MTP	-	-	-	-	-	2	6	9	9	-

Note: As of Sep 30th, 2020

Technology Development

Developments by process nodes

12" Fabs	Production	Development	IP Type	Process Type
5/6nm	0	4	OTP, PUF	FF
7/10nm	2	1	OTP, PUF	FF, FF+
12/16nm	3	5	OTP, PUF	FF, FF+
22/28nm	31	11	OTP, PUF, MTP	LP/ULP/ULL, HPC/HPC+, HV-OLED, DRAM, SOI
40nm	15	5	OTP, PUF, MTP	LP/ULP, E-Flash, HV-DDI/OLED
55/65nm	25	15	OTP, PUF, MTP	LP/ULP, E-Flash, HV-DDI/OLED, DRAM, CIS, BCD, PM
80/90nm	16	15	OTP, MTP	HV-DDI/OLED, LP, Generic, BCD, CIS
0.11/0.13um	17	3	OTP, MTP	HV-DDI, BCD, Generic
0.18um	1	3	OTP	BCD, Generic
Total	110	62		

8" Fabs	Development	IP Type	Process Type
90nm	1	ОТР	HV-DDI, LL, BCD
0.11/0.13um	17	OTP, MTP	HV/HV-MR, BCD, LP/LL, CIS, Green, Flash, SOI, Generic
0.152/0.16/0.18um	17	OTP, MTP	HV/HV-MR, BCD, LP/LL, CIS, Green, Generic
0.25um	1	ОТР	BCD
0.35um	0	ОТР	UHV
Total	36		

Note: As of Sep 30th, 2020

