



NeoBit

Floating Gate OTP Silicon IP

eMemory's NeoBit is an one-time programmable memory solution using floating gate technology. It is compatible to logic processes and does not require additional mask layers. One of the world's most widely used OTP.



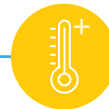
High Yield & Most Widely Used

High production record and suitable for major foundries with CMOS technologies



Fast Time to Market

Easy porting to derivative processes



High Temperature

AEC-Q100 Grade 0 compliant



High Reliability

ISO26262 ASIL D certification

Highlights

- Industry's most widely embedded NVM device
- Suitable for all derivative CMOS technologies
- 0.5µm~55nm



High Performance & Low Cost

Small IP footprint with low and wide supply voltage

NeoFuse

Anti-Fuse OTP Silicon IP

eMemory's NeoFuse is an anti-fuse technology offering reliable, secure and high yield embedded OTP/PMTTP function for advanced and more-than-Moore technology nodes. The IP is tailored for the applications with low power, high reliability and high security needs, such as IoT, STB, FPGA, SSD, DRAM, ISP and Automotive.



High Yield & Low Cost

Small IP footprint with high manufacturing yield, low failure ppm in field programming



Fast Time to Market

Strong R&D and technical support to clients for smooth and fast production



High Security

Alliances with international security partners for secure OTP IP



High Reliability

Durable in harsh environments (High Temperature and EM radiation)

Highlights

- Industry's first proven OTP NVM IP in leading 7/10/16nm FinFET technology; Also verified in HPC+/ULP/FD-SOI/Flash/HV/BCD/DRAM /CIS, etc.
- As an invisible and irreversible data storage, suitable for high security, high yield and high reliability against the harsh environments
- 0.15µm~5nm



Low Power & High Performance

Extended power supply range to meet low power and high performance requirements